



FIG. 1A

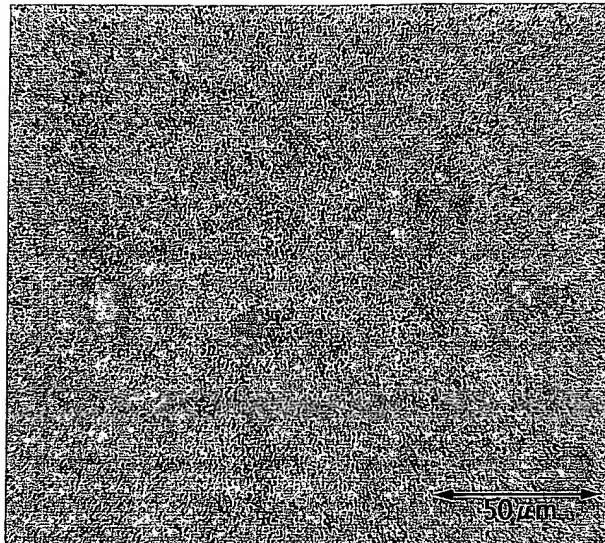


FIG. 1B

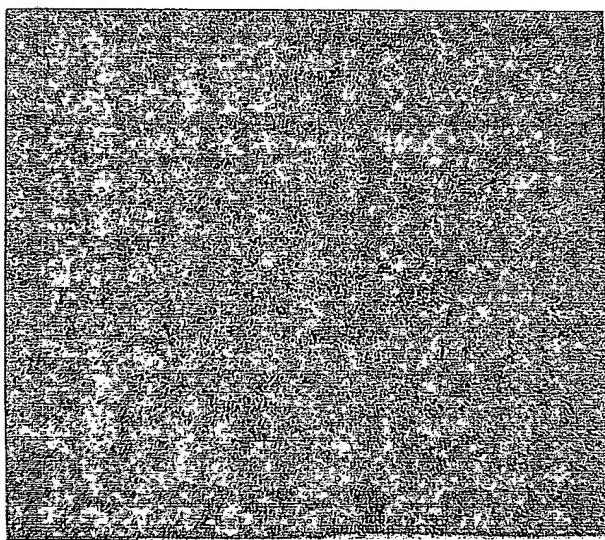


FIG. 1C

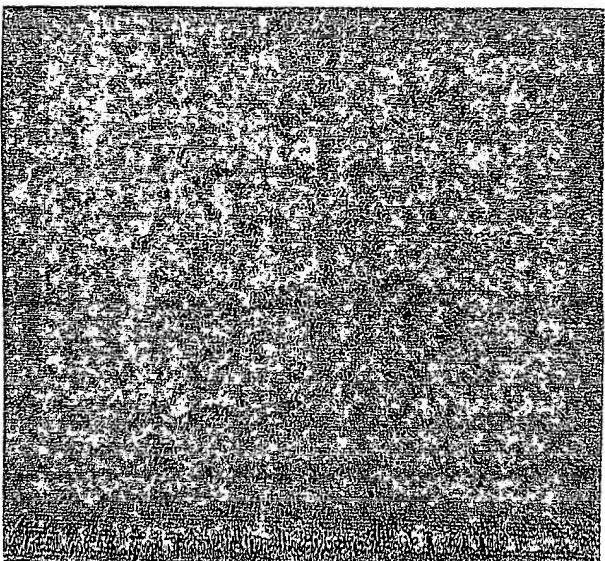


FIG. 2A

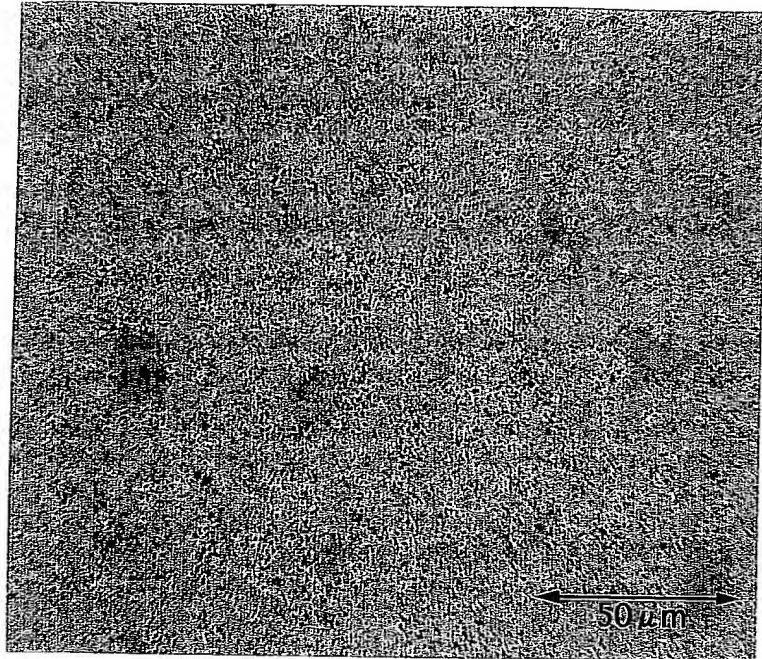


FIG. 2B

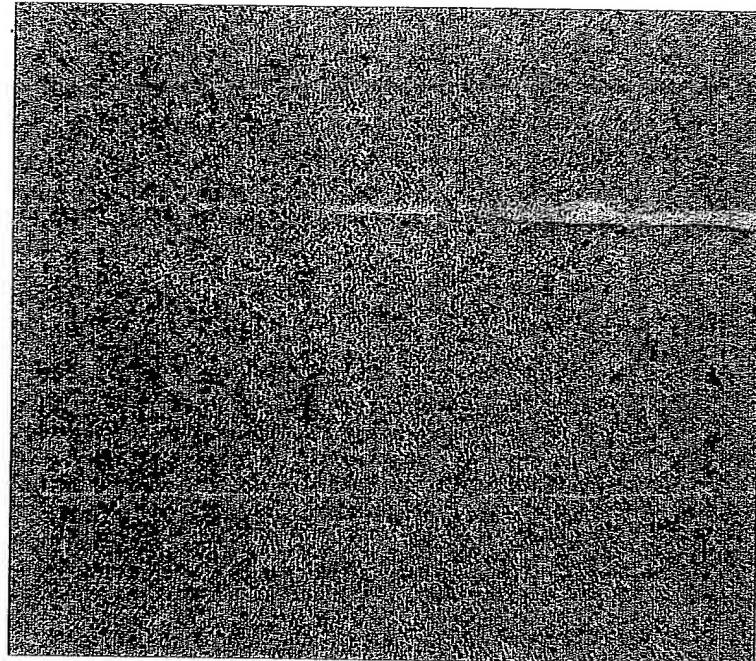


FIG. 3A

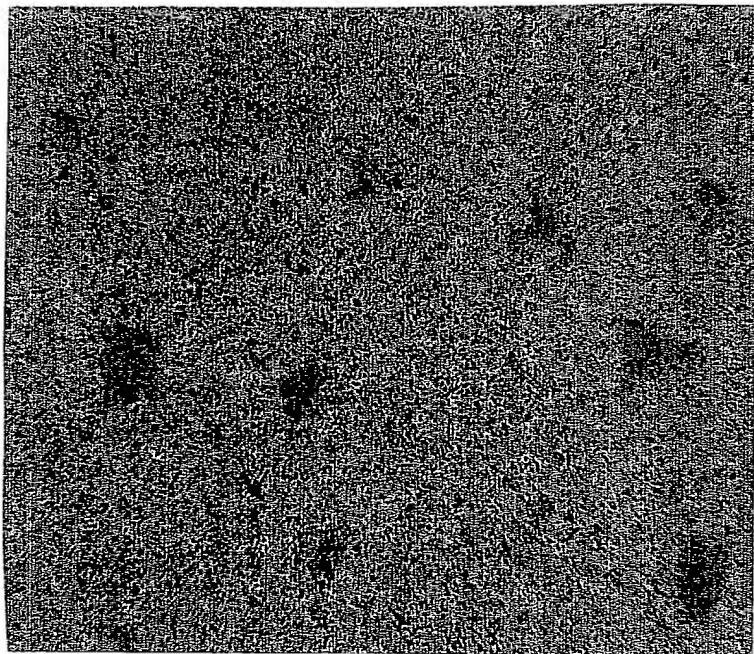
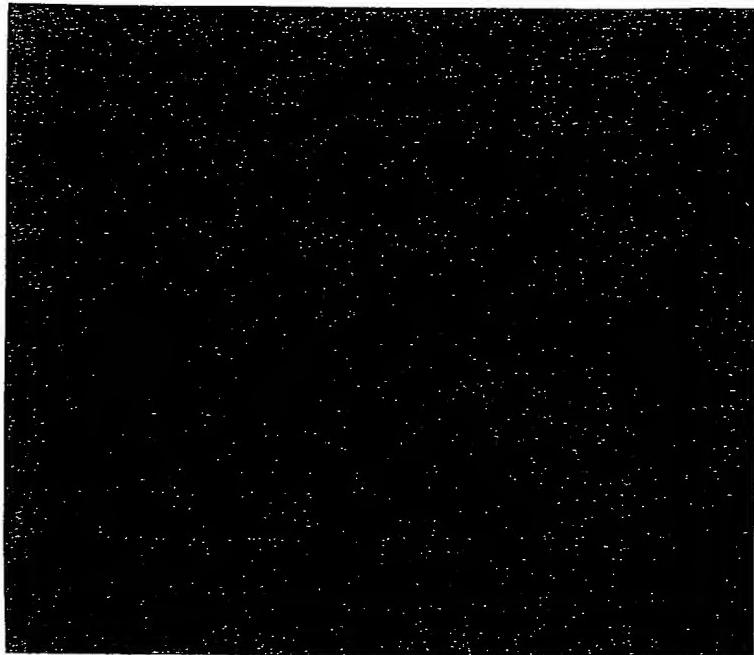


FIG. 3B



+

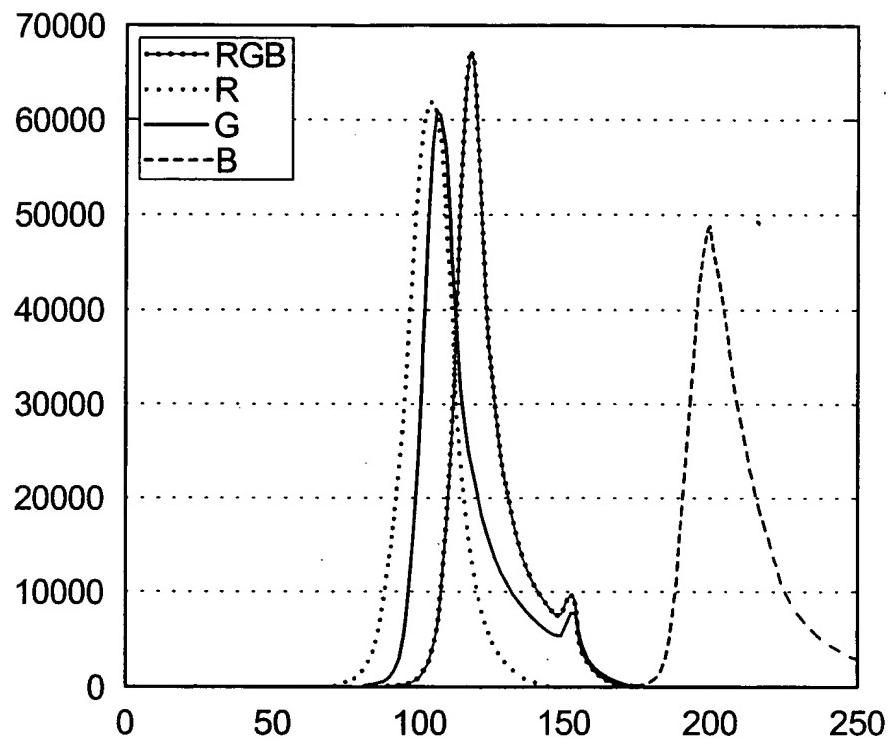


FIG. 4

FIG. 5A

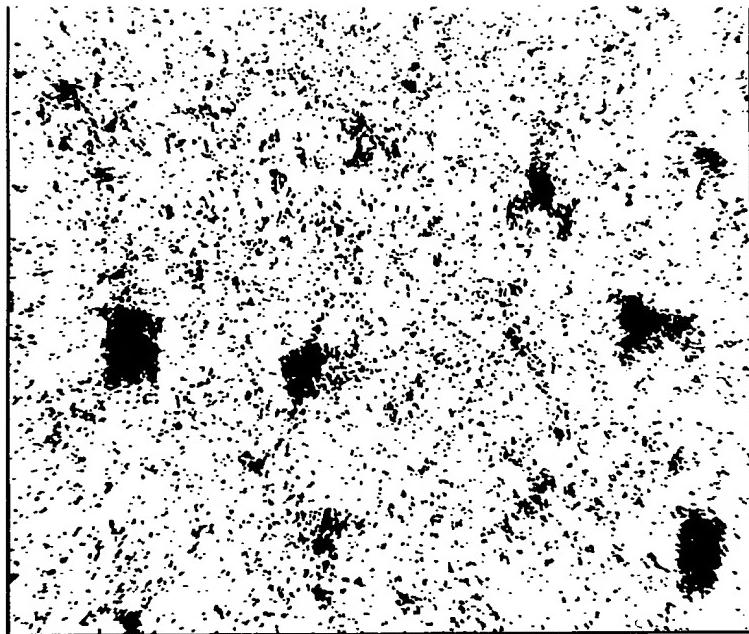
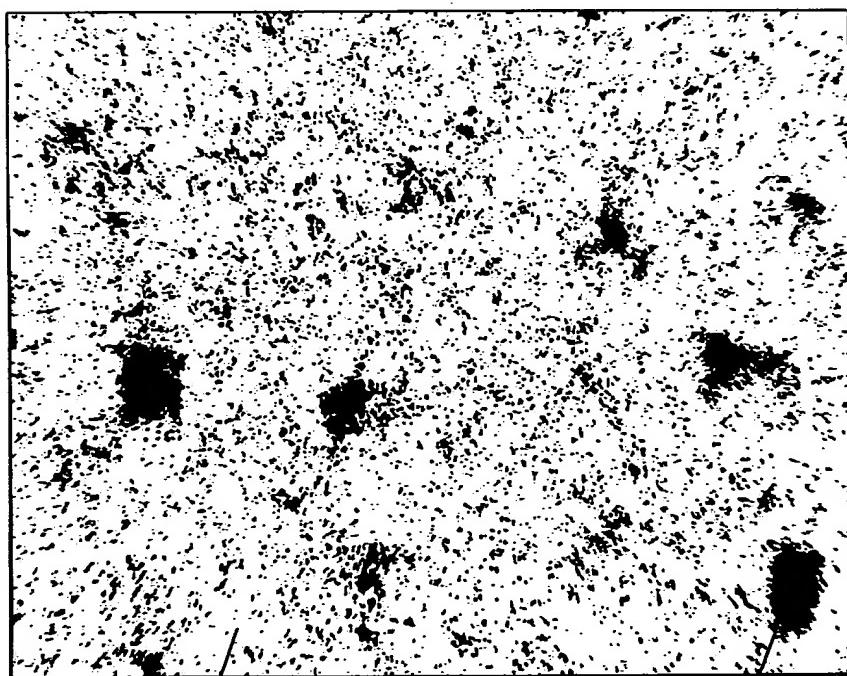


FIG. 5B



5001

5002

+

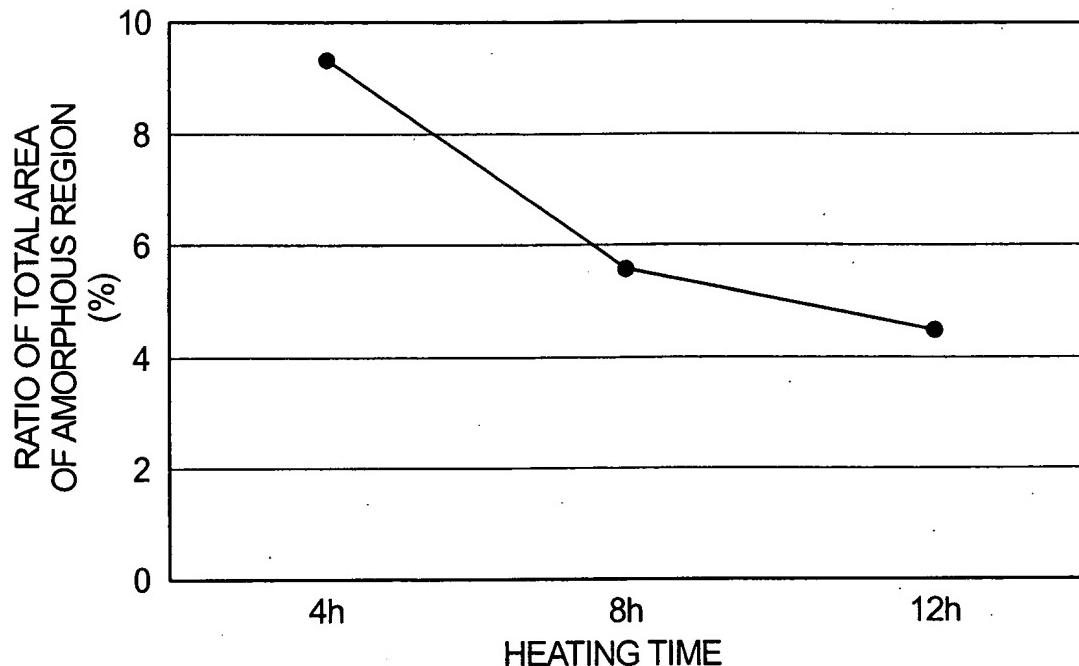


FIG. 6A

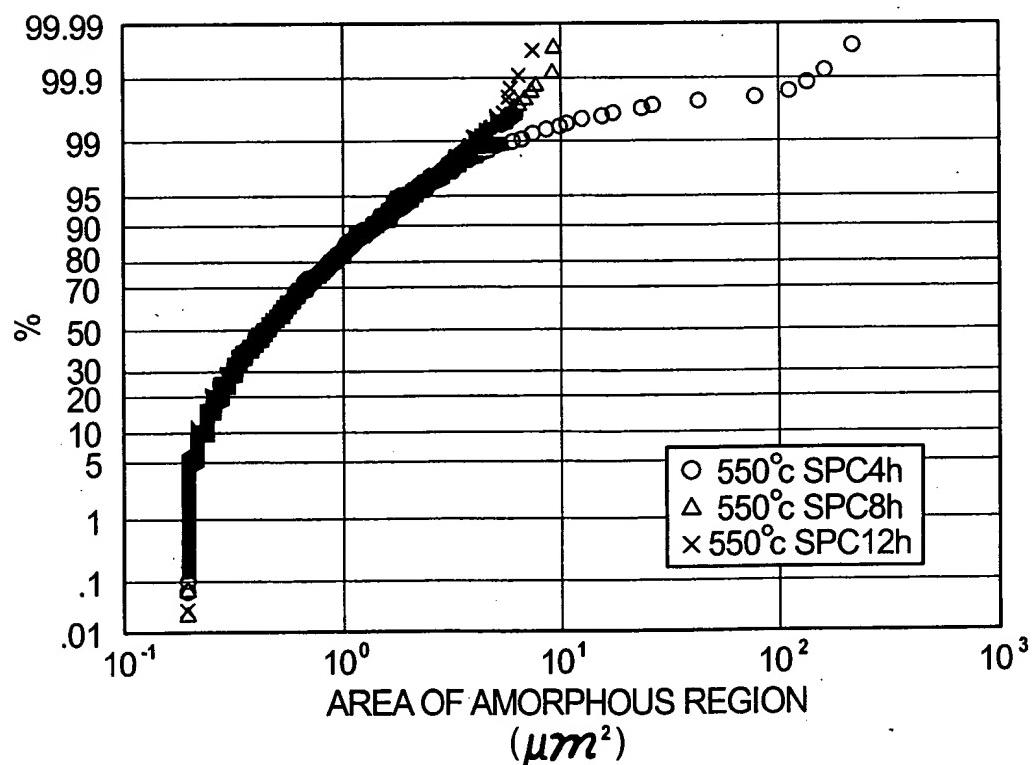


FIG. 6B

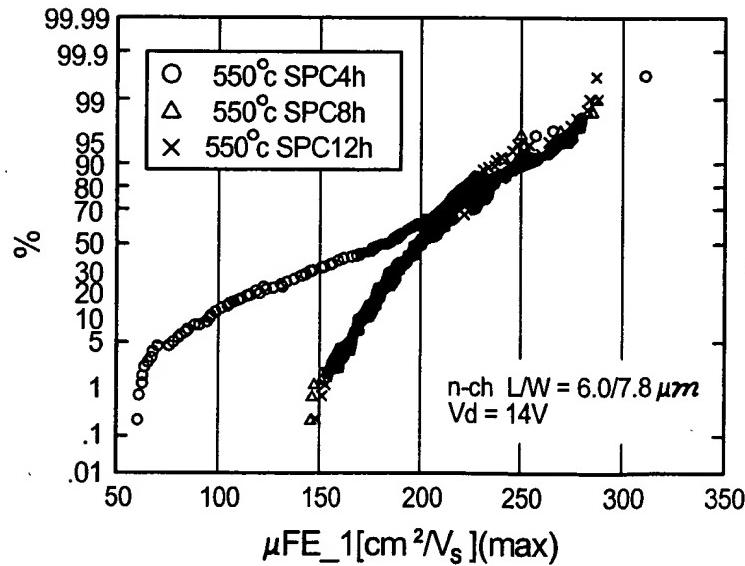
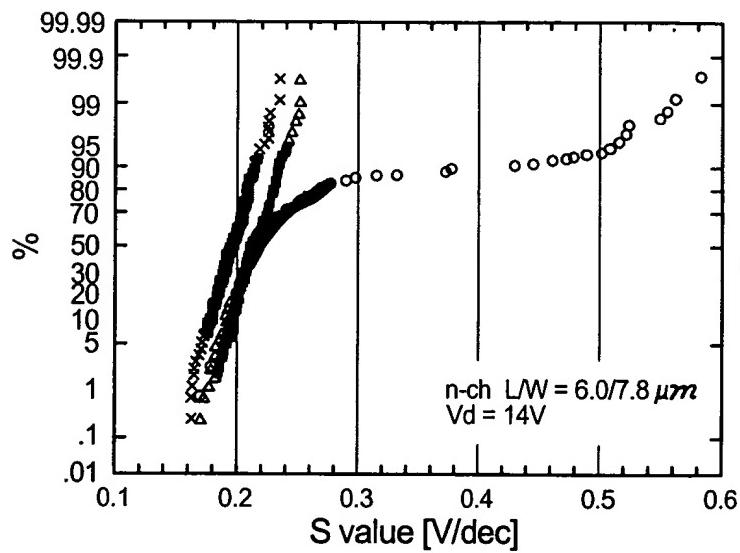
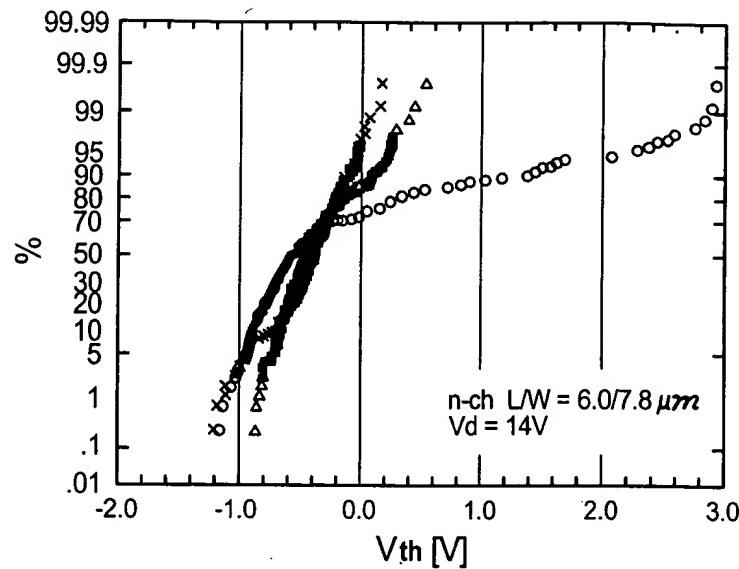


FIG. 8B

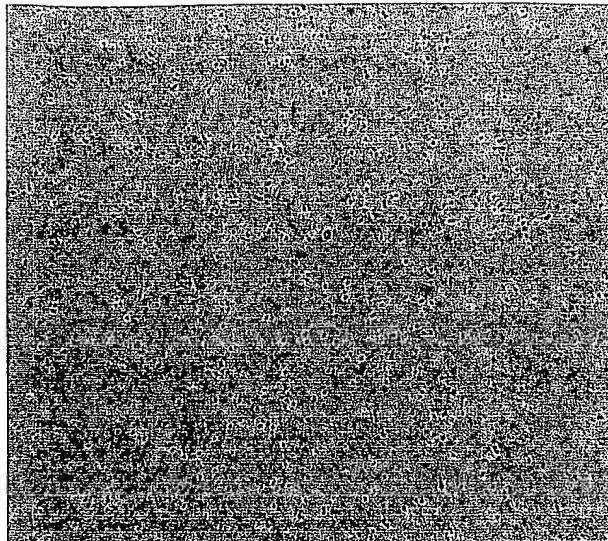
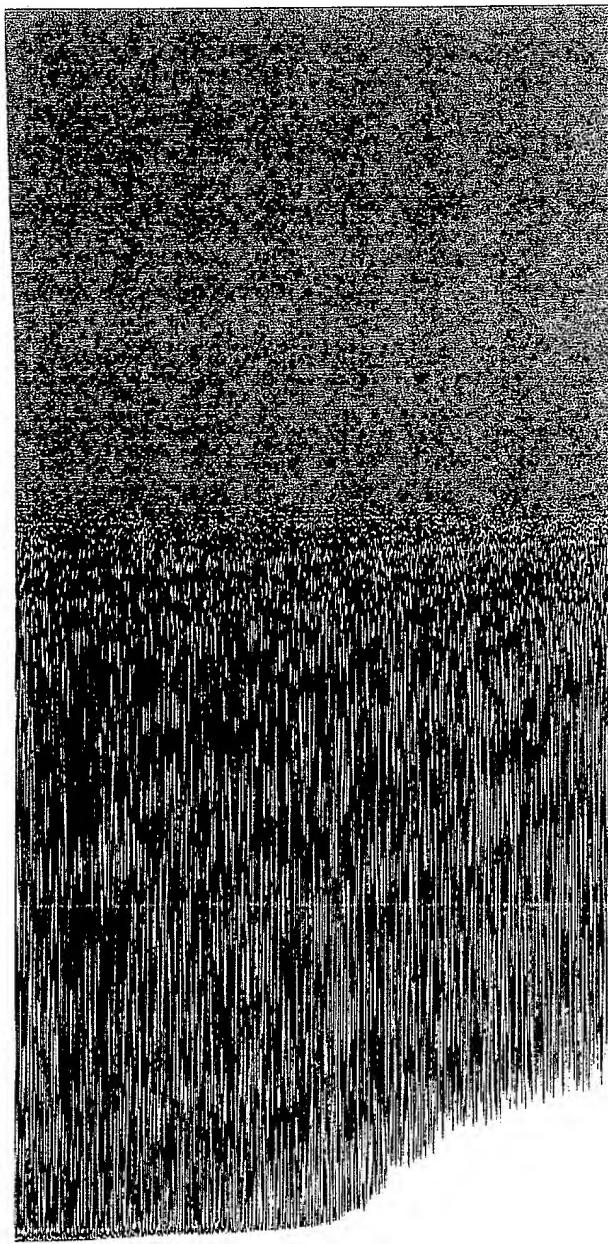


FIG. 8C



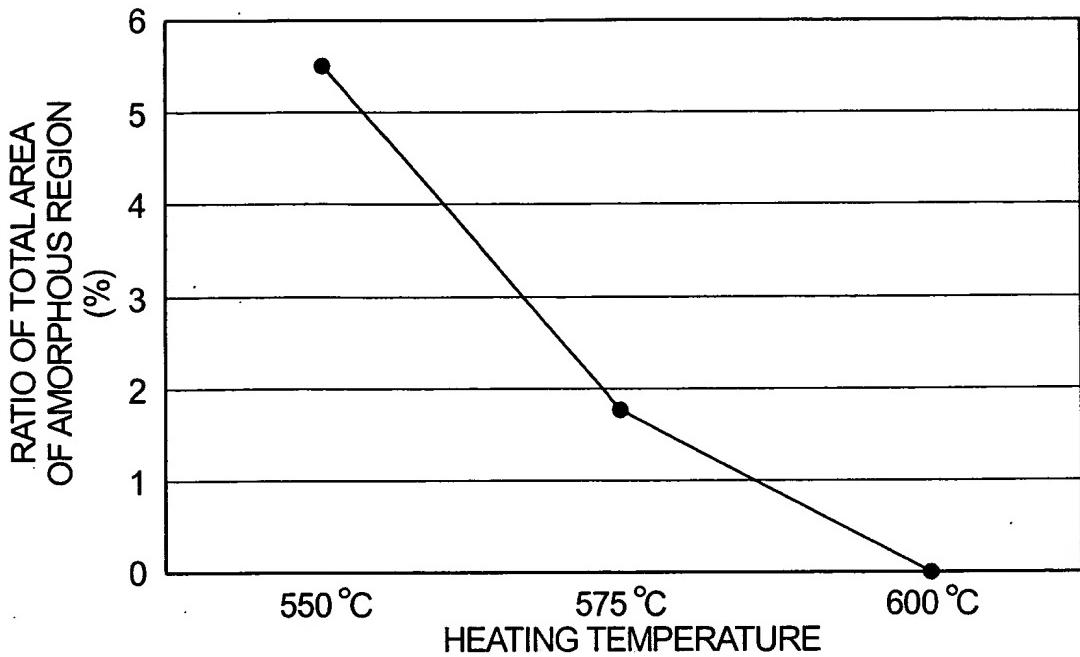


FIG. 9A

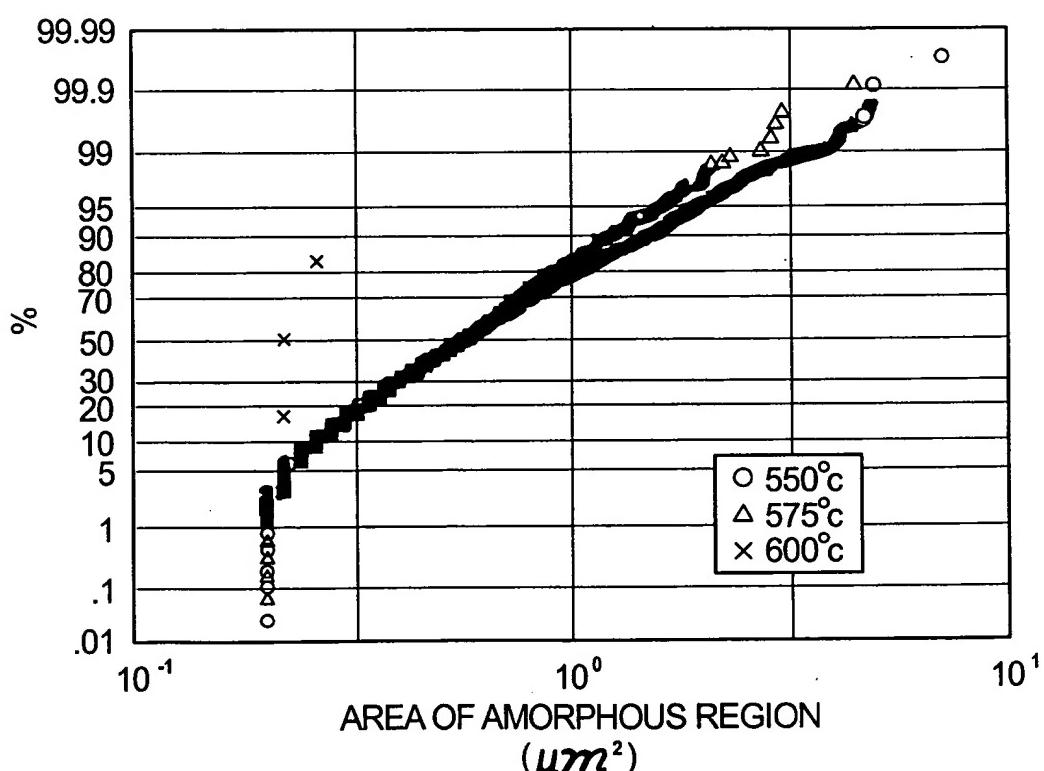
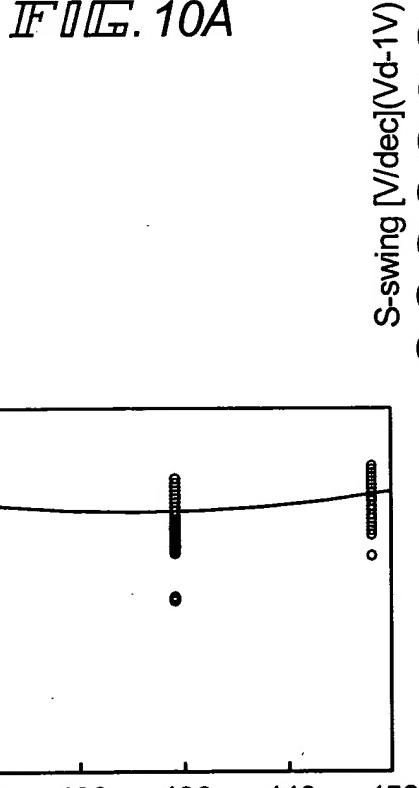
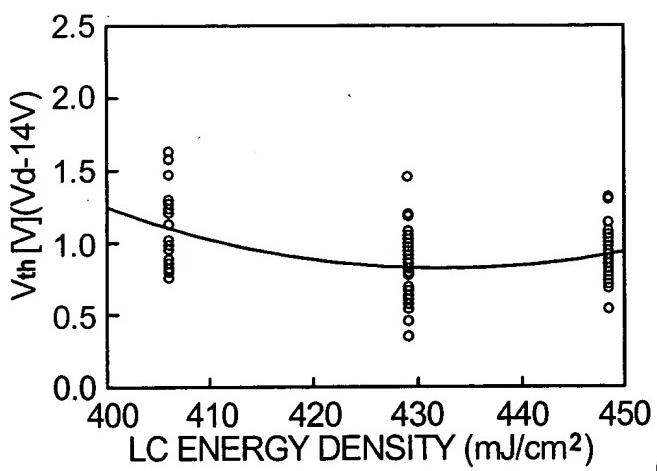


FIG. 9B



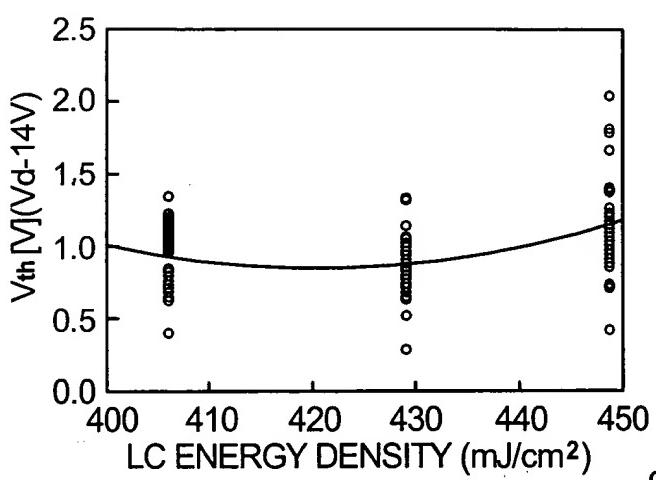


FIG. 10E

FIG. 10F

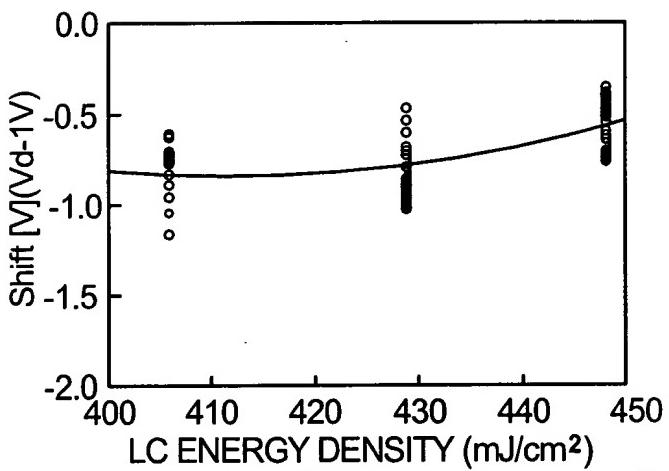
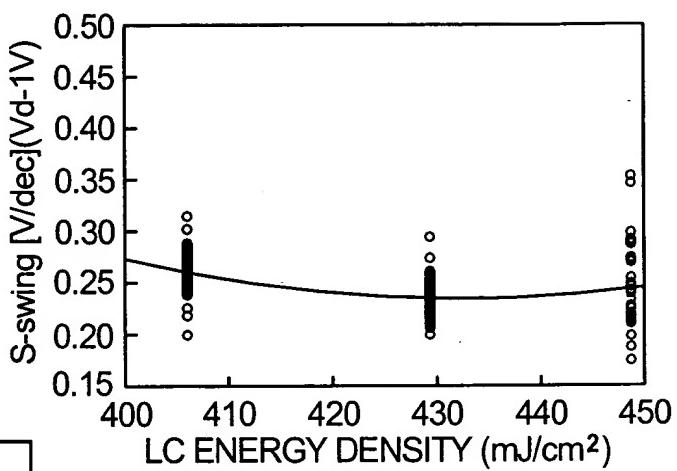
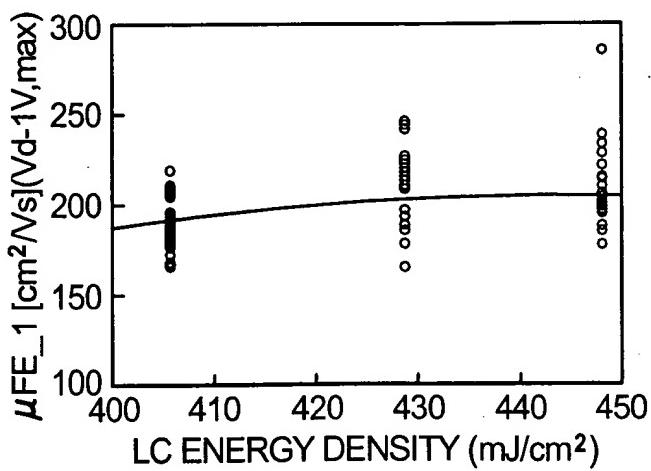


FIG. 10G

FIG. 10H



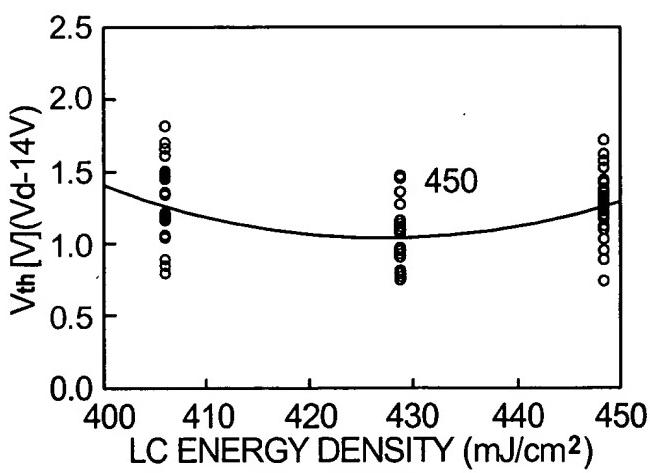


FIG. 11A

FIG. 11B

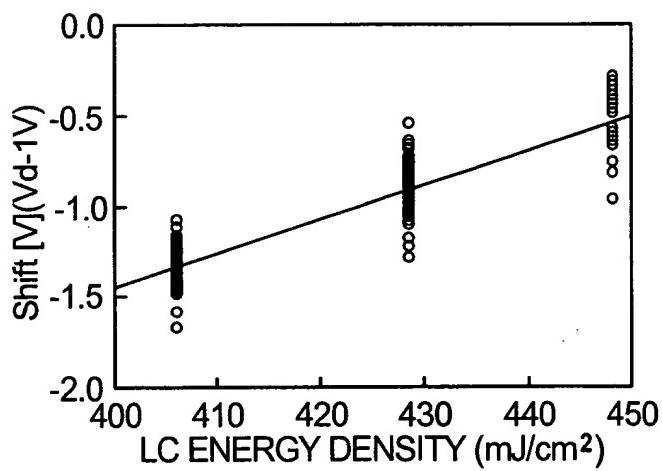
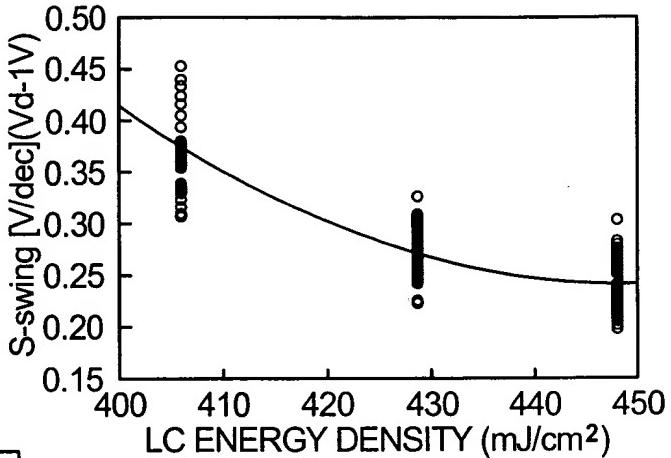
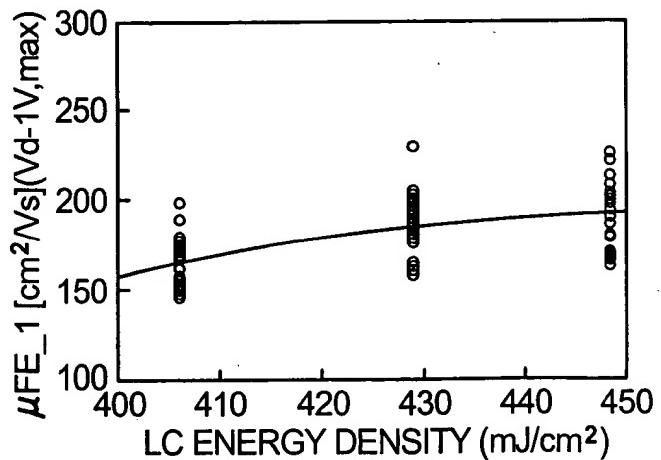


FIG. 11C

FIG. 11D



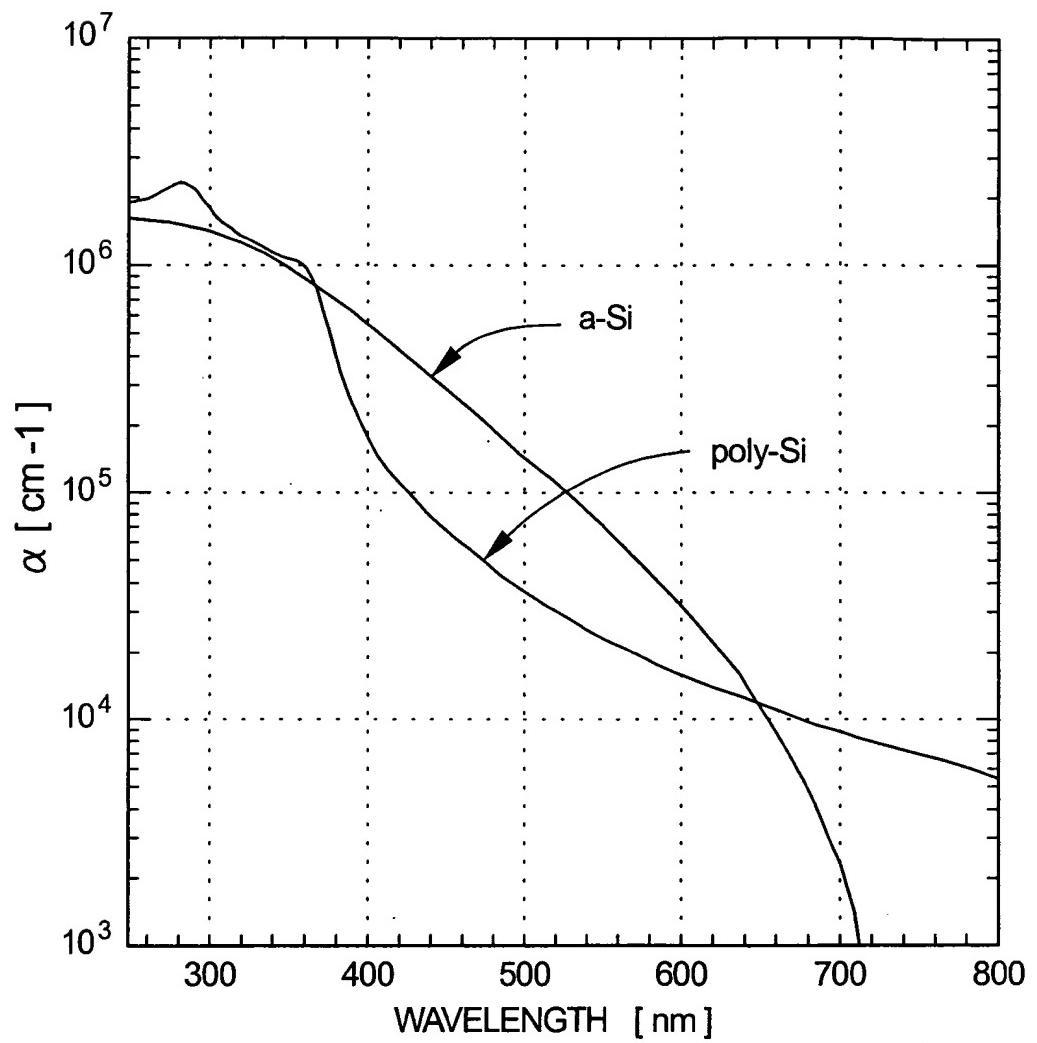
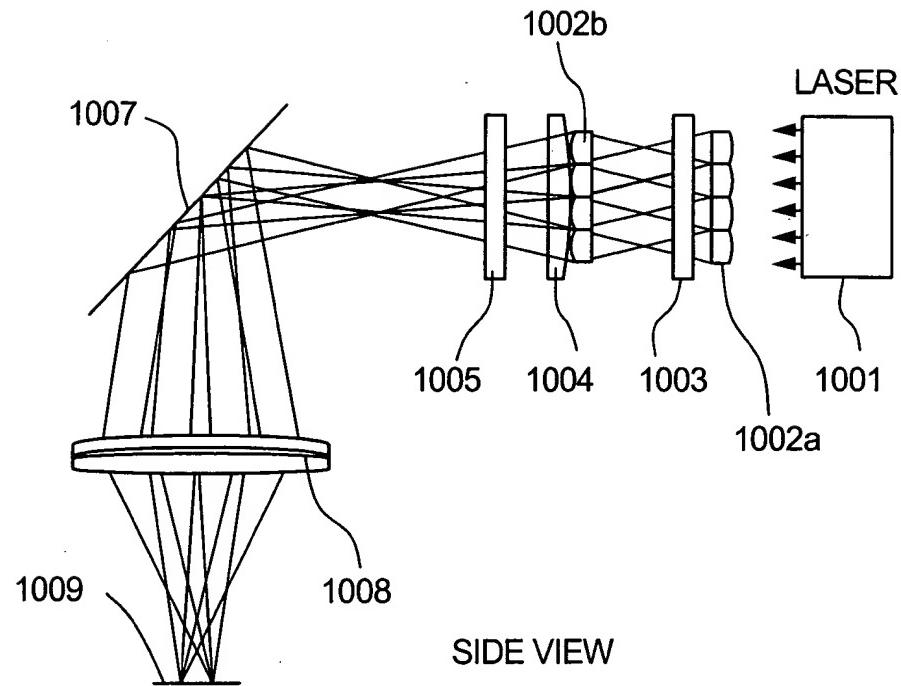
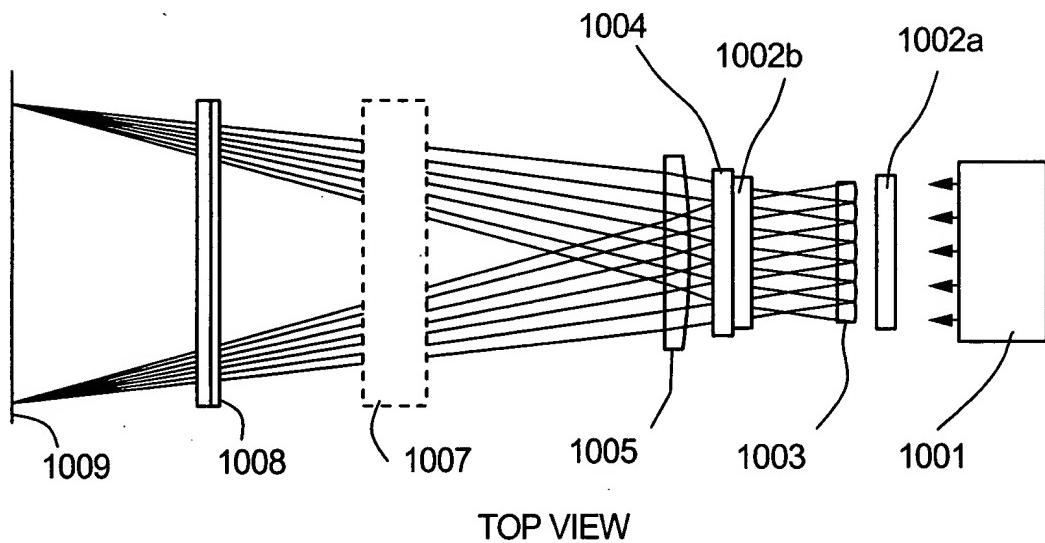


FIG. 12

+



SIDE VIEW



TOP VIEW

FIG. 13

+

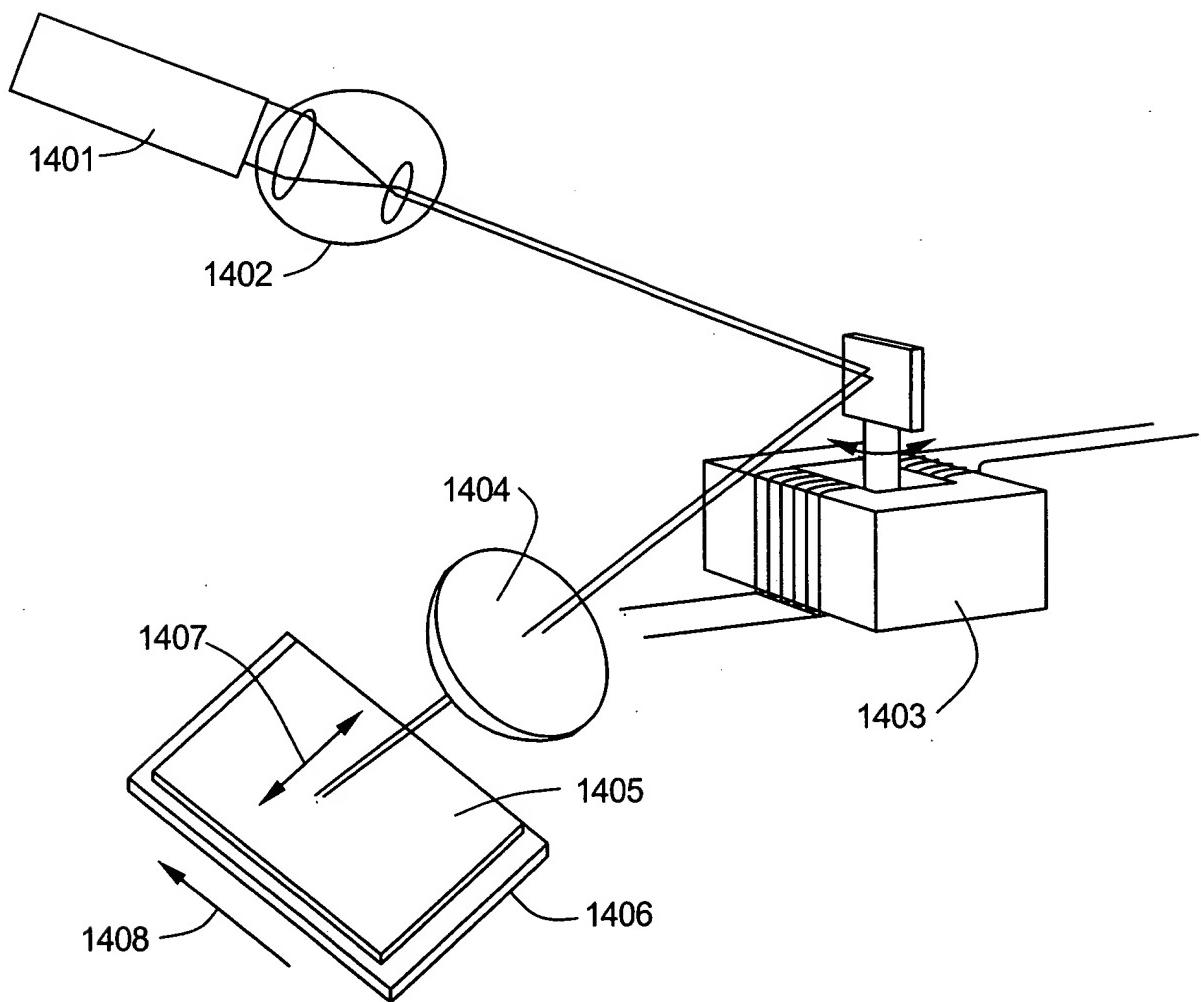
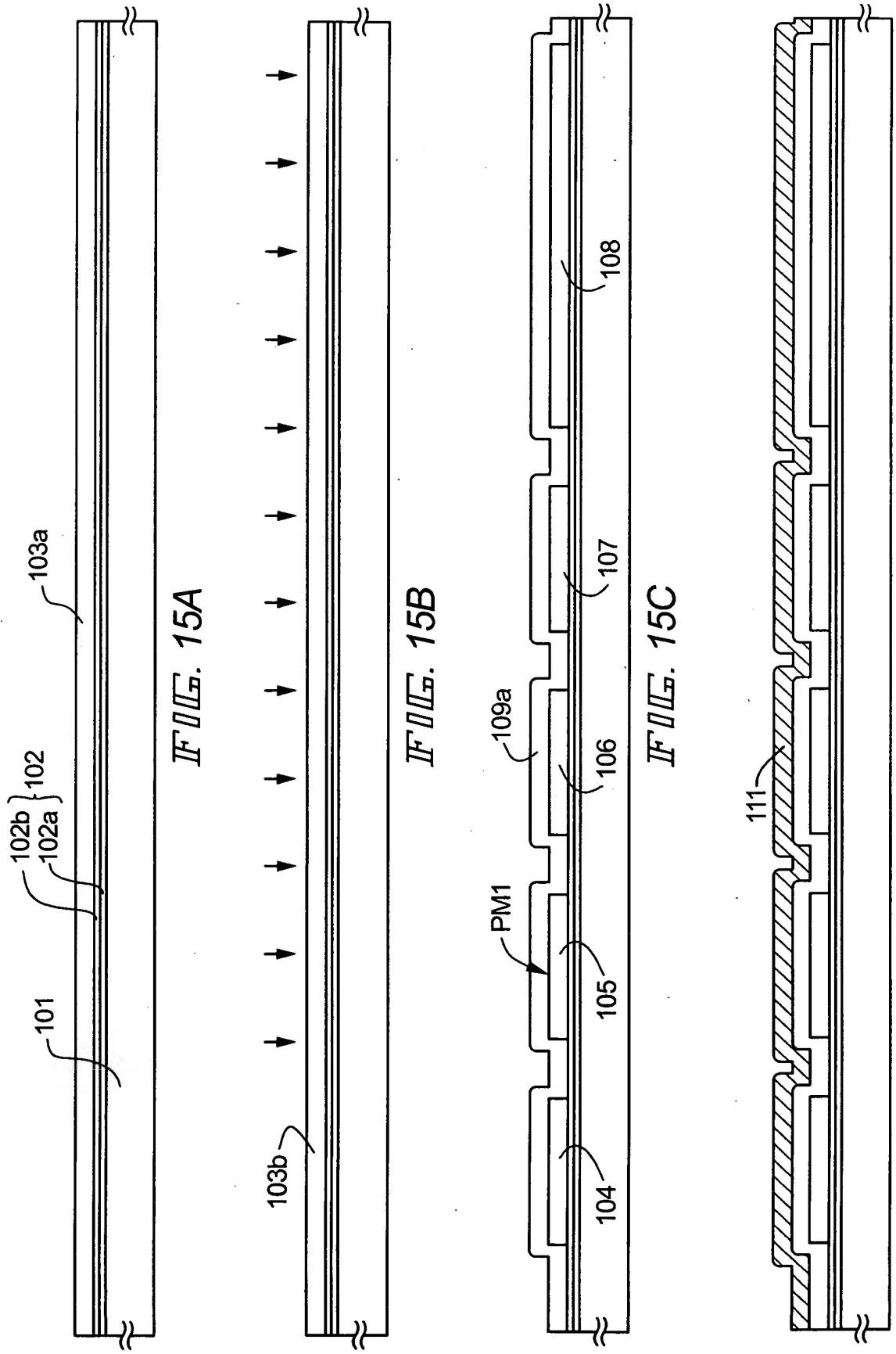


FIG. 14



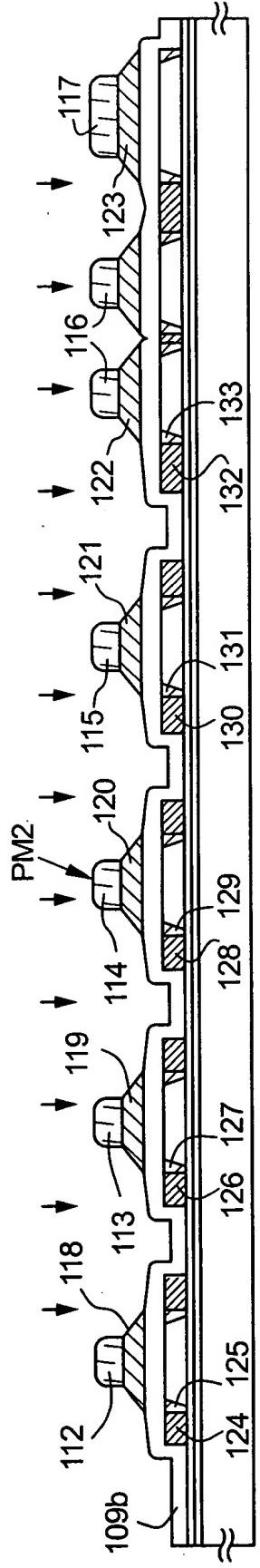


FIG. 16A

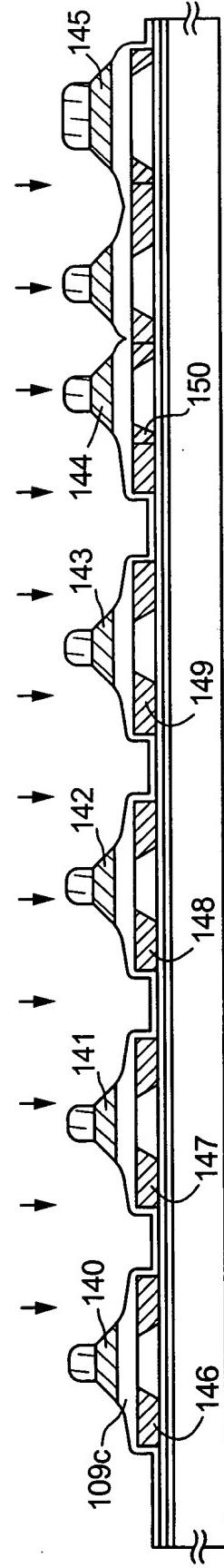


FIG. 16B

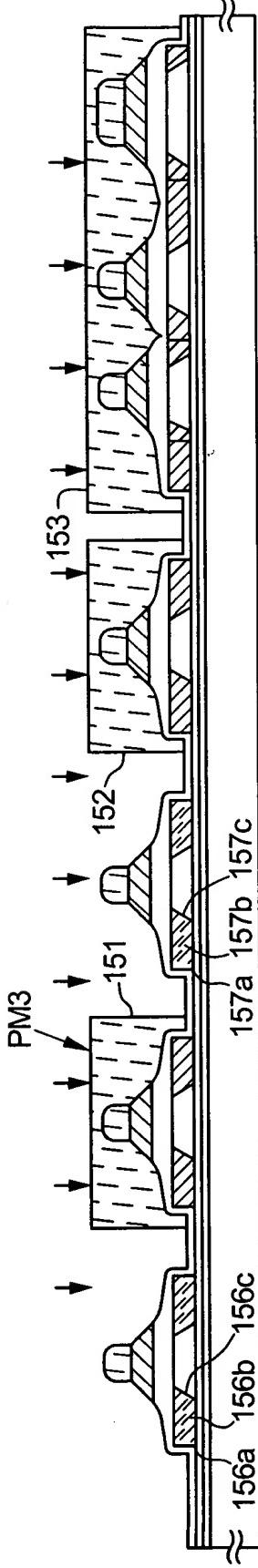


FIG. 16C

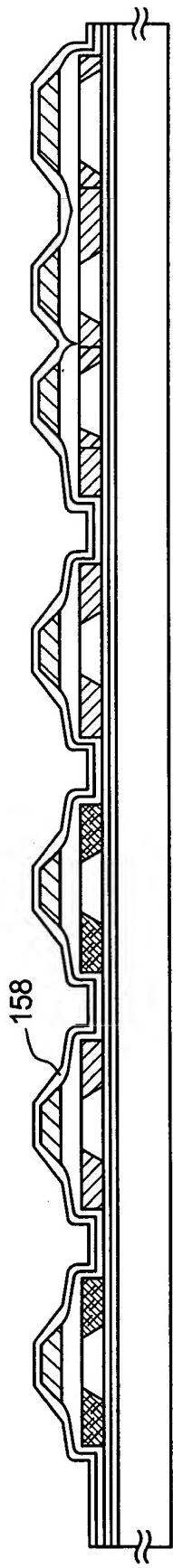
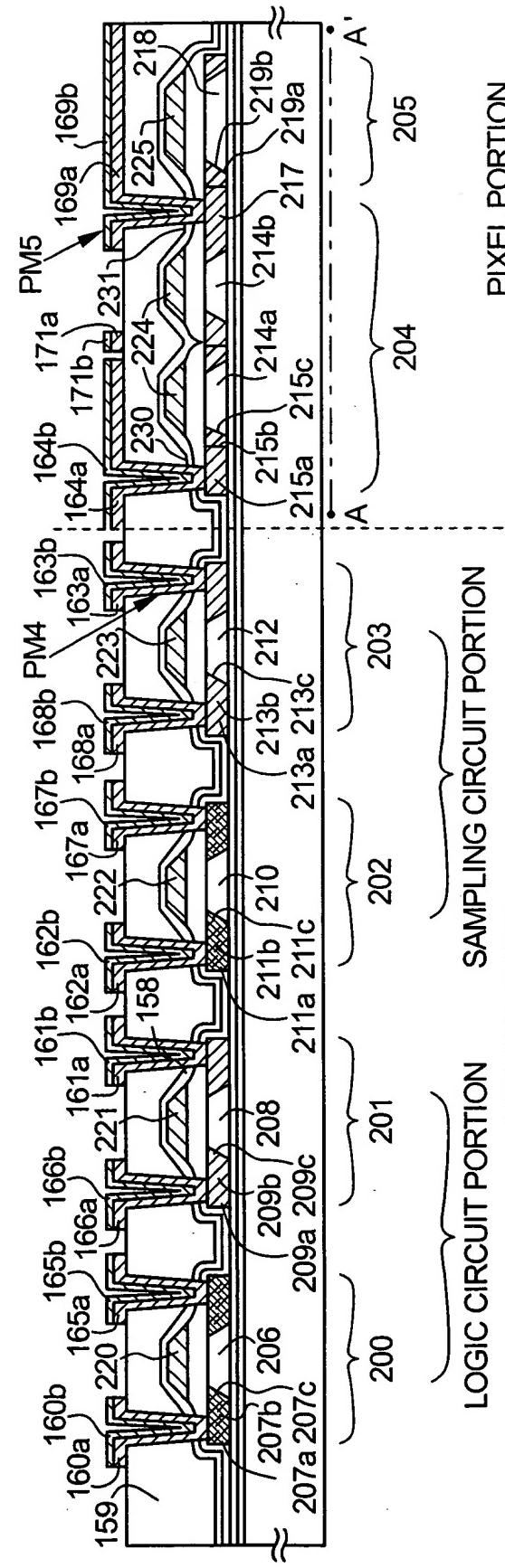


FIG. 17A



PIXEL PORTION

SAMPLING CIRCUIT PORTION

DRIVER CIRCUIT

FIG. 17B

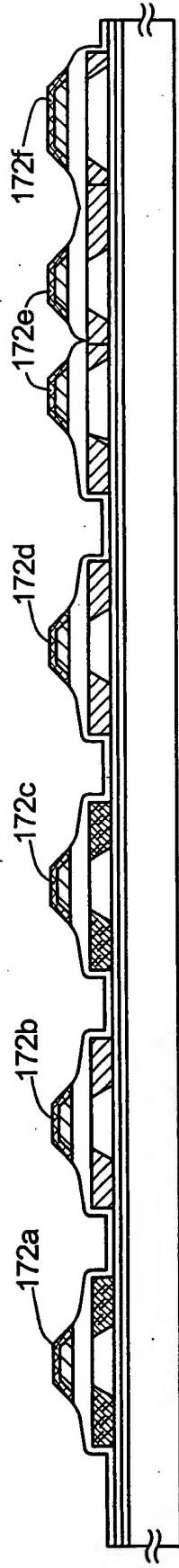


FIG. 18A

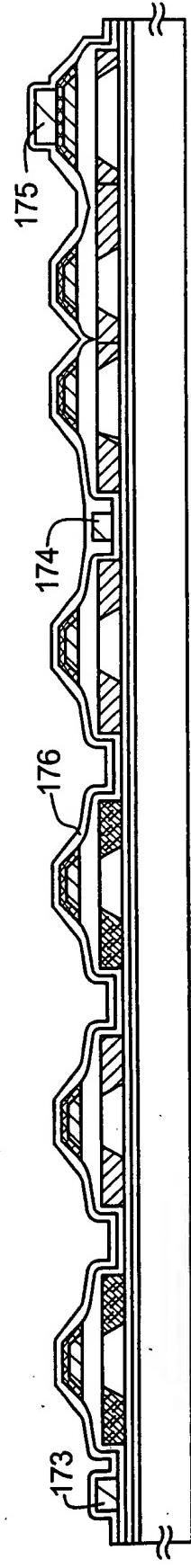
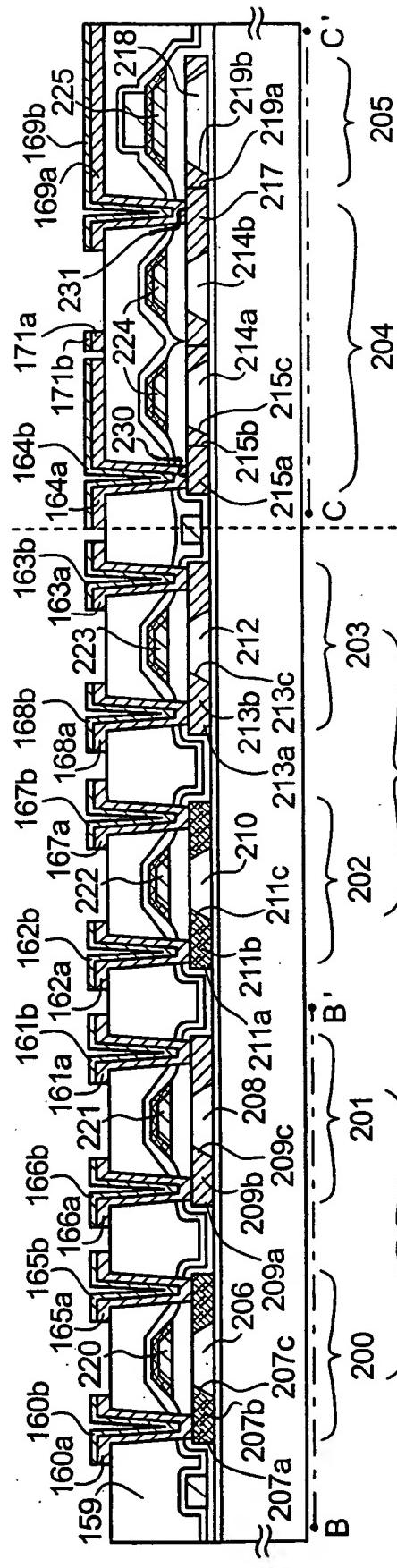


FIG. 18B



LOGIC CIRCUIT PORTION
SAMPLING CIRCUIT PORTION
DRIVER CIRCUIT
PIXEL PORTION

FIG. 18C

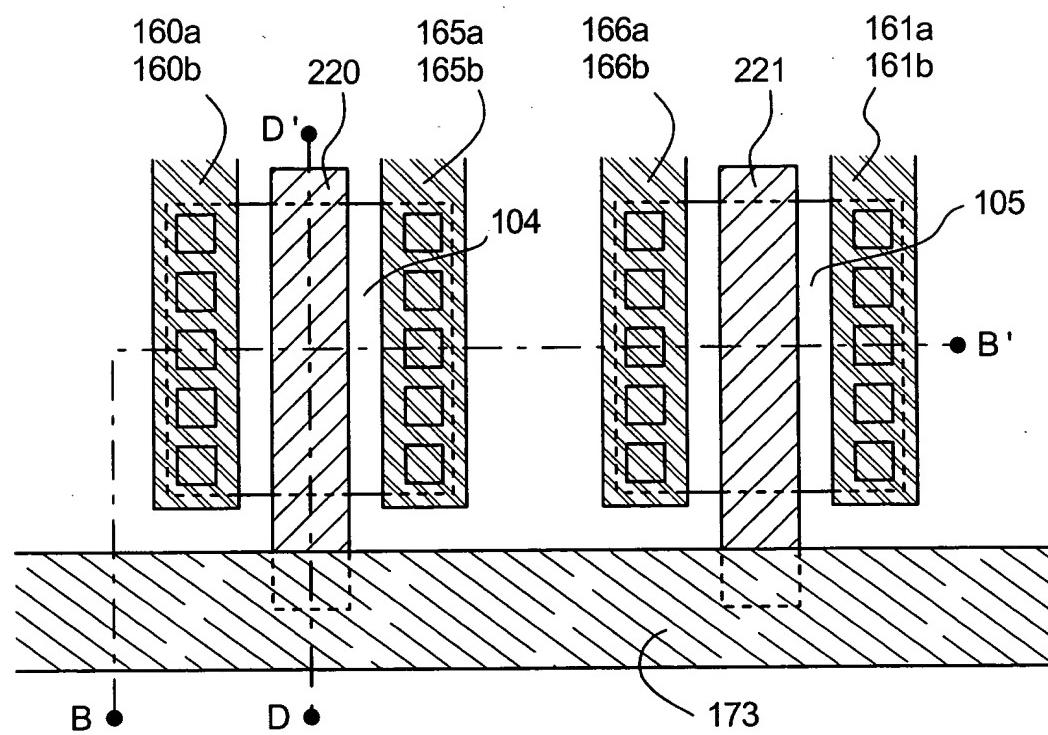


FIG. 19A

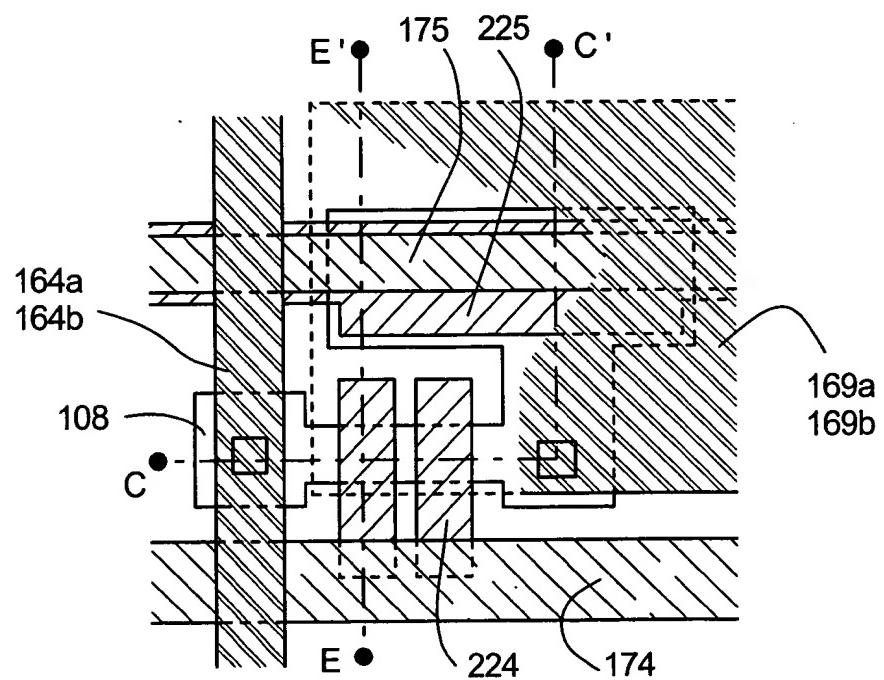


FIG. 19B

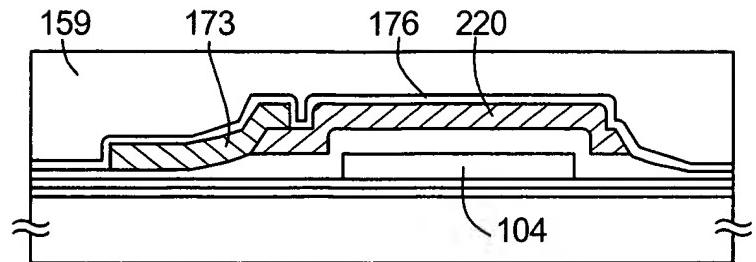


FIG. 20A

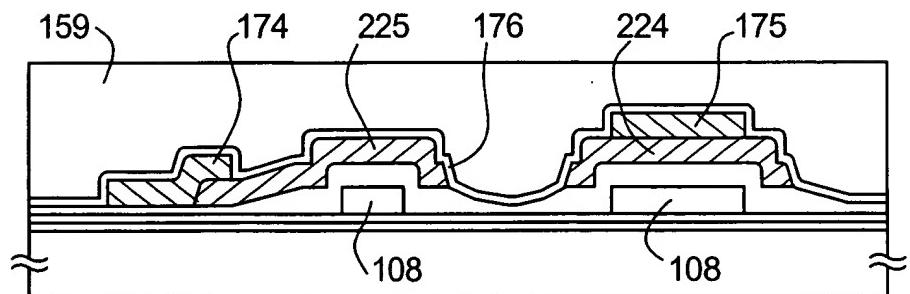
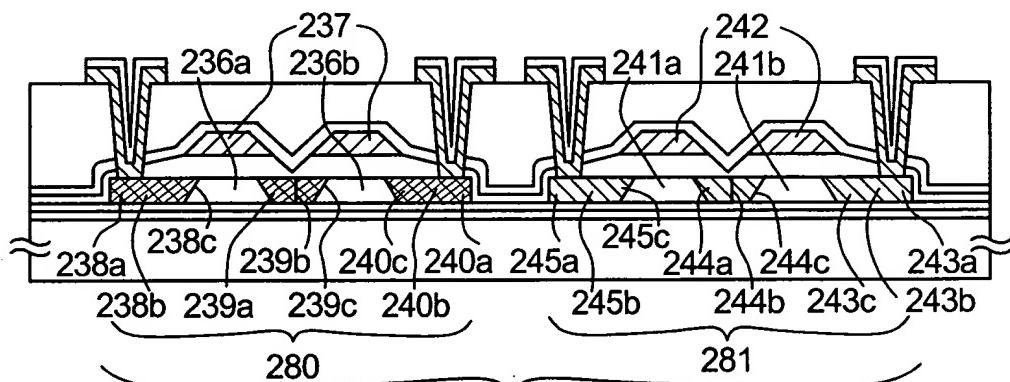
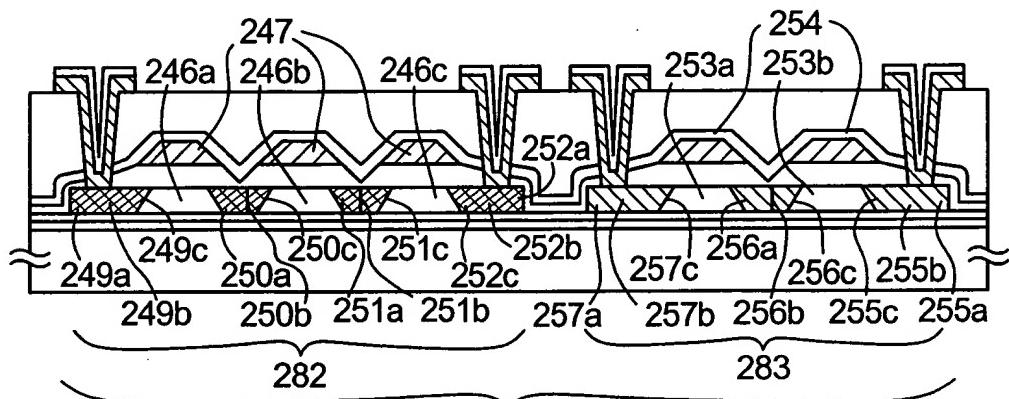


FIG. 20B



LOGIC CIRCUIT PORTION

FIG. 21A



SAMPLING CIRCUIT PORTION

FIG. 21B

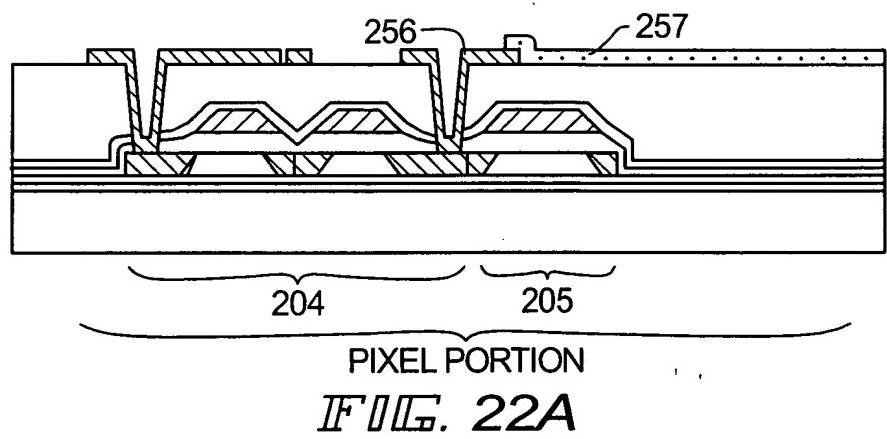


FIG. 22A

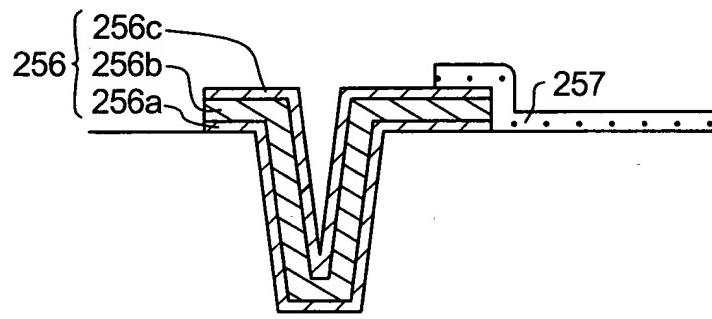


FIG. 22B

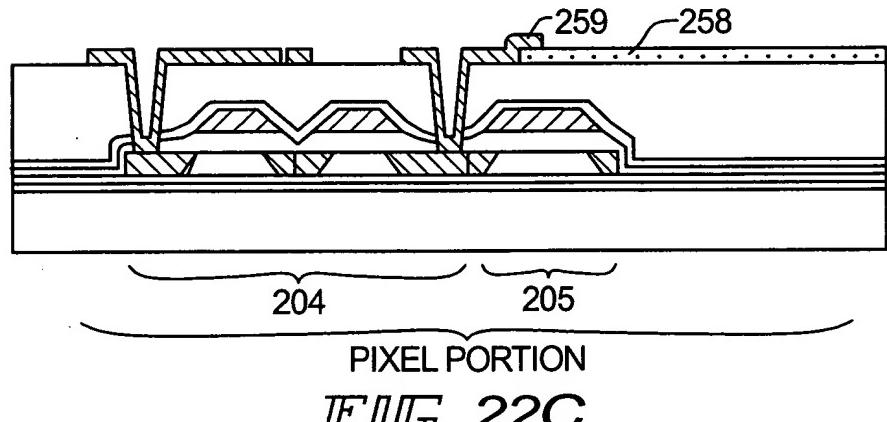


FIG. 22C

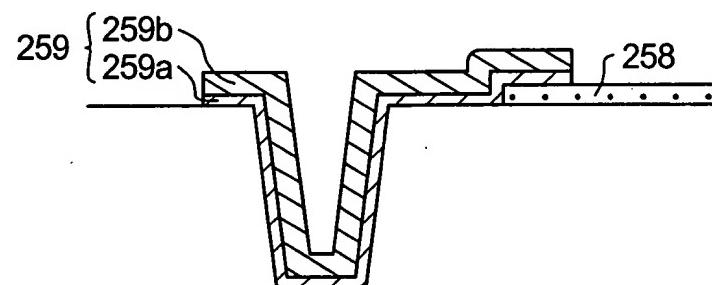


FIG. 22D

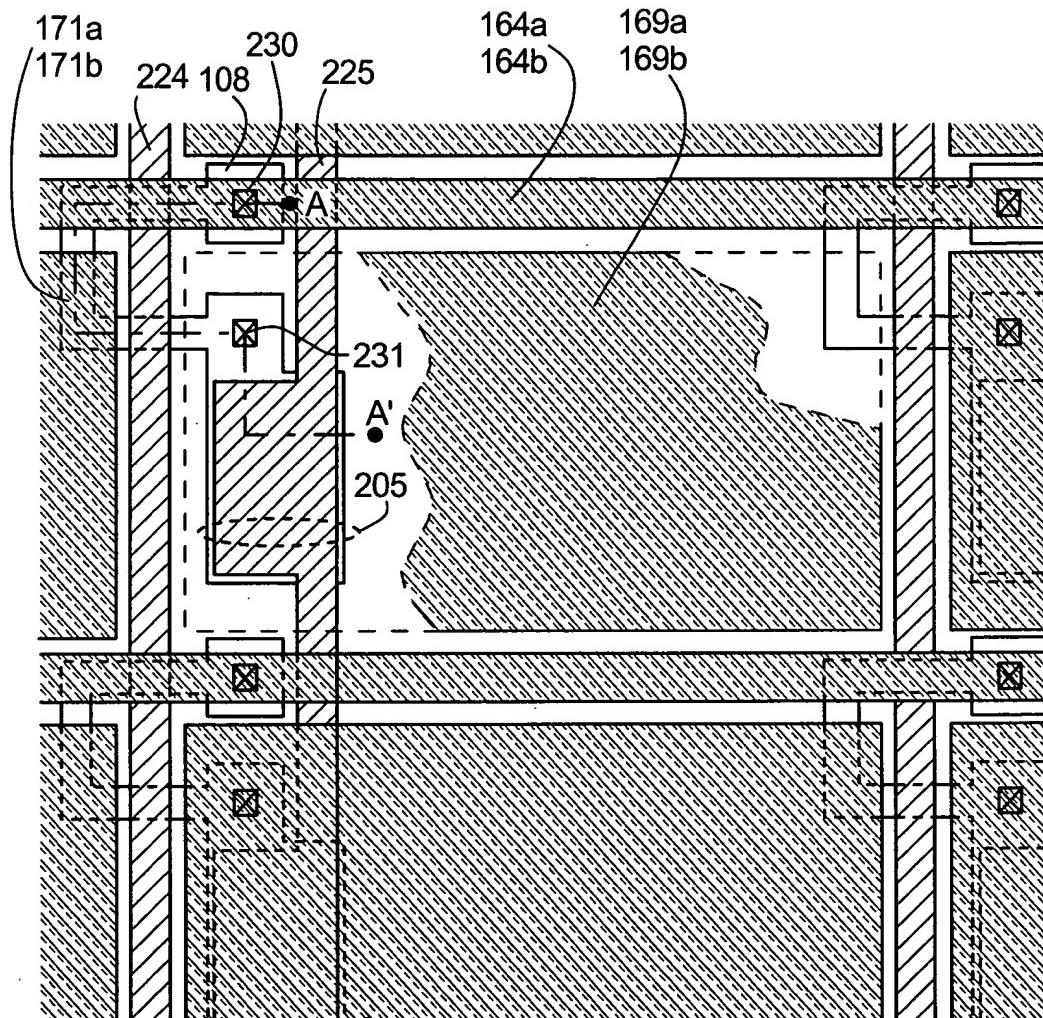


FIG. 23

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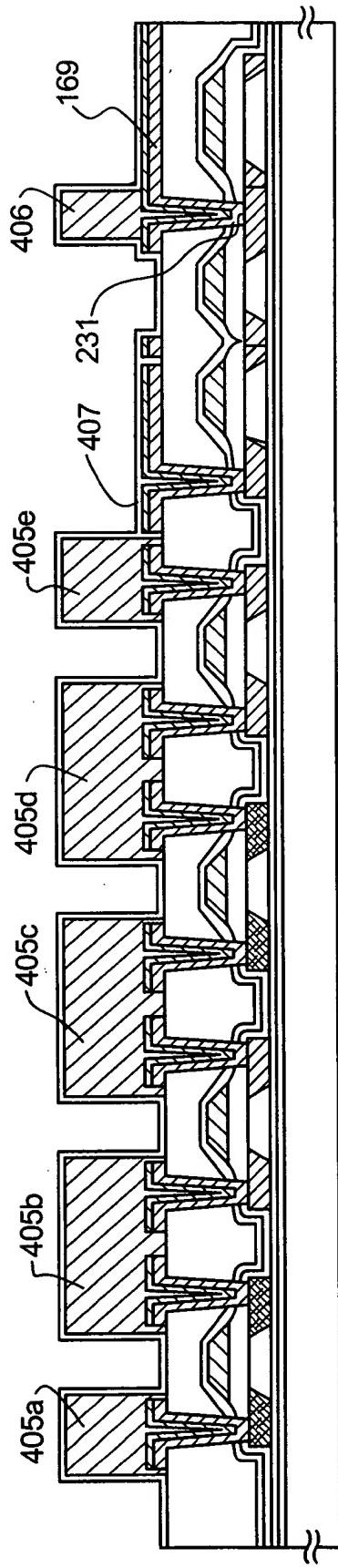
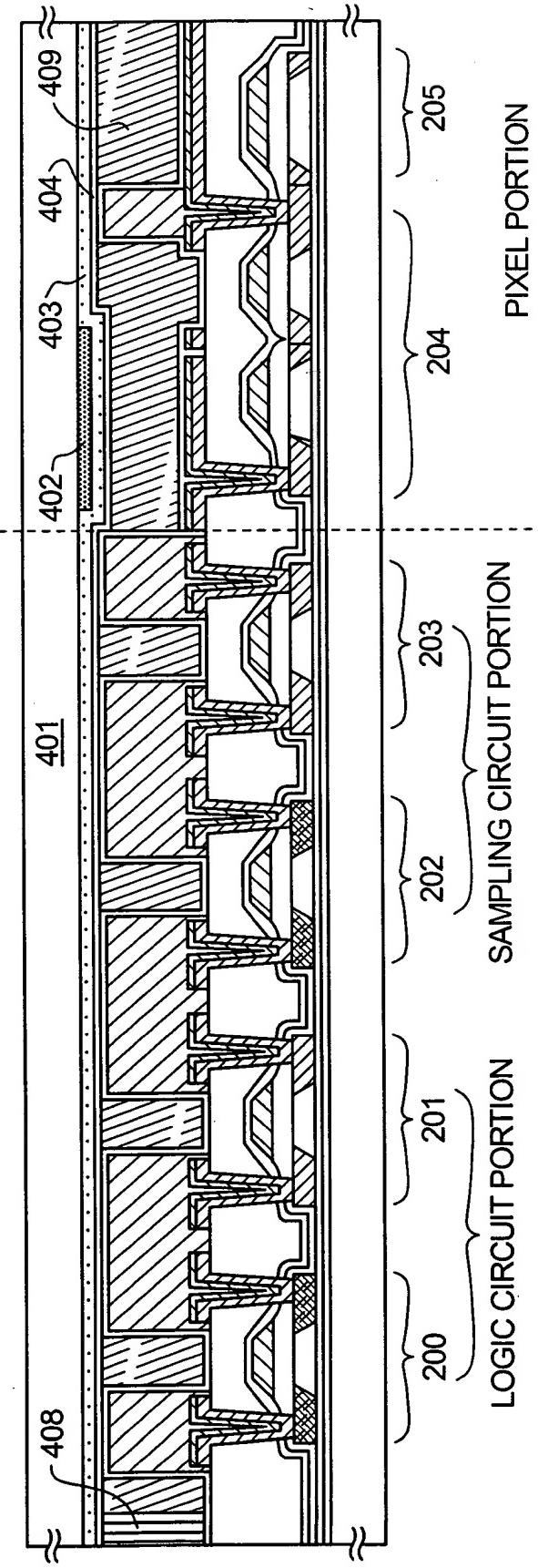


FIG. 24A



LOGIC CIRCUIT PORTION
SAMPLING CIRCUIT PORTION
DRIVER CIRCUIT
PIXEL PORTION

FIG. 24B

+

+

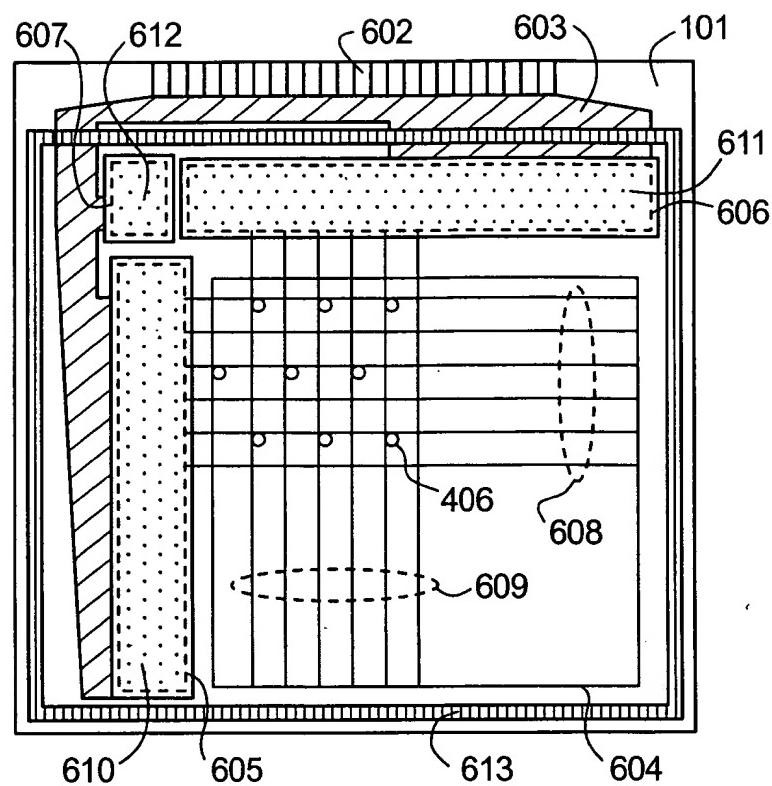


FIG. 25

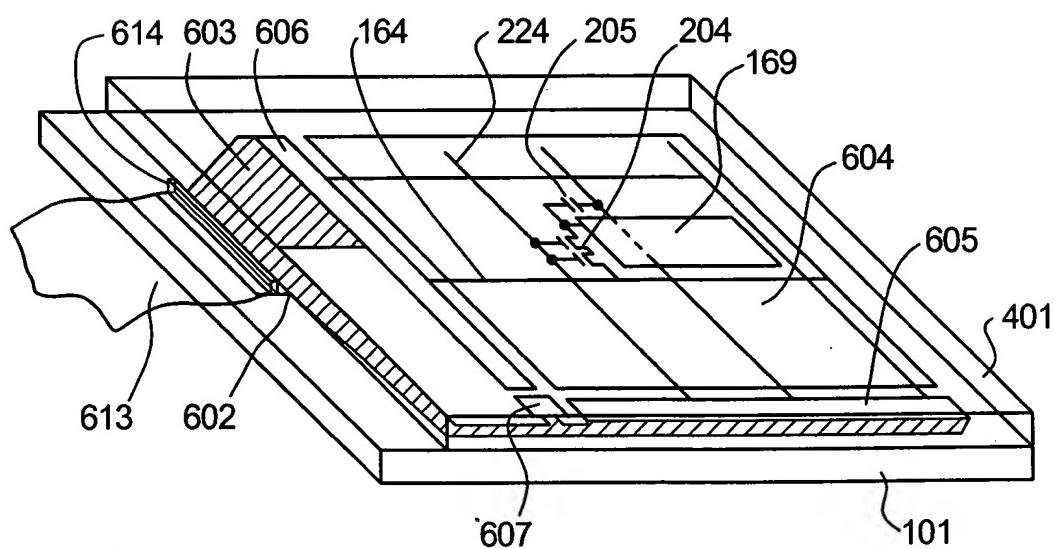


FIG. 26

+

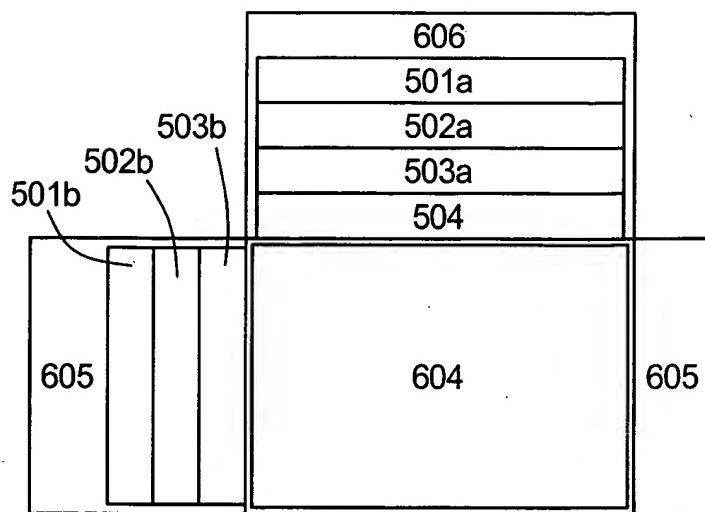
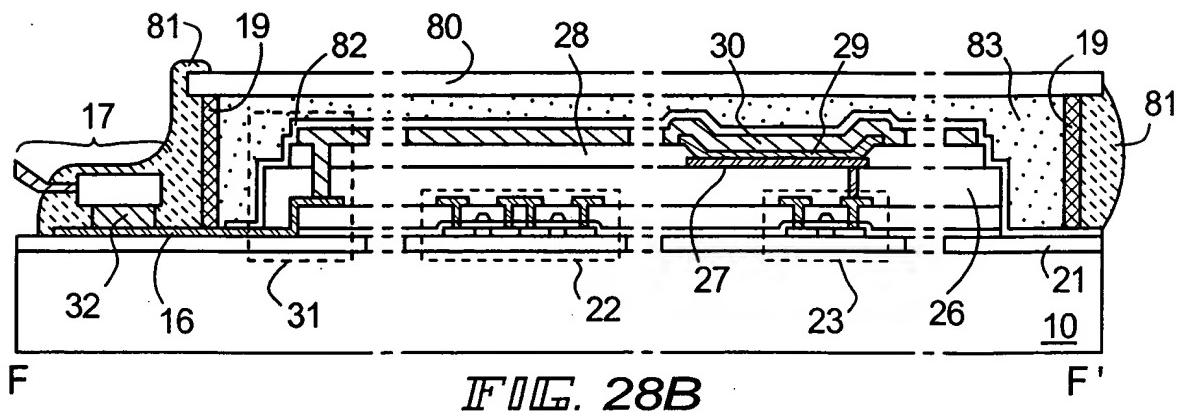
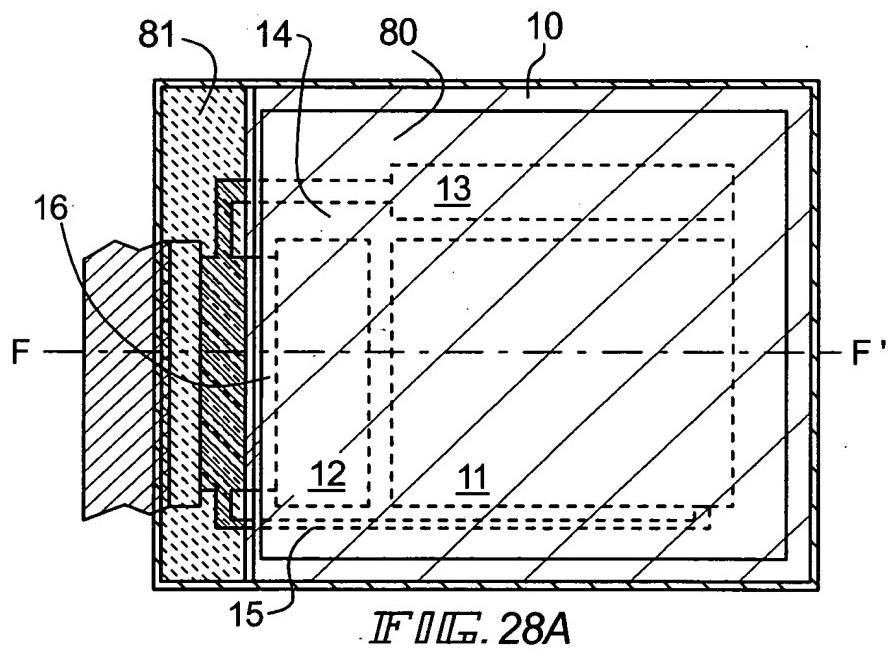


FIG. 27



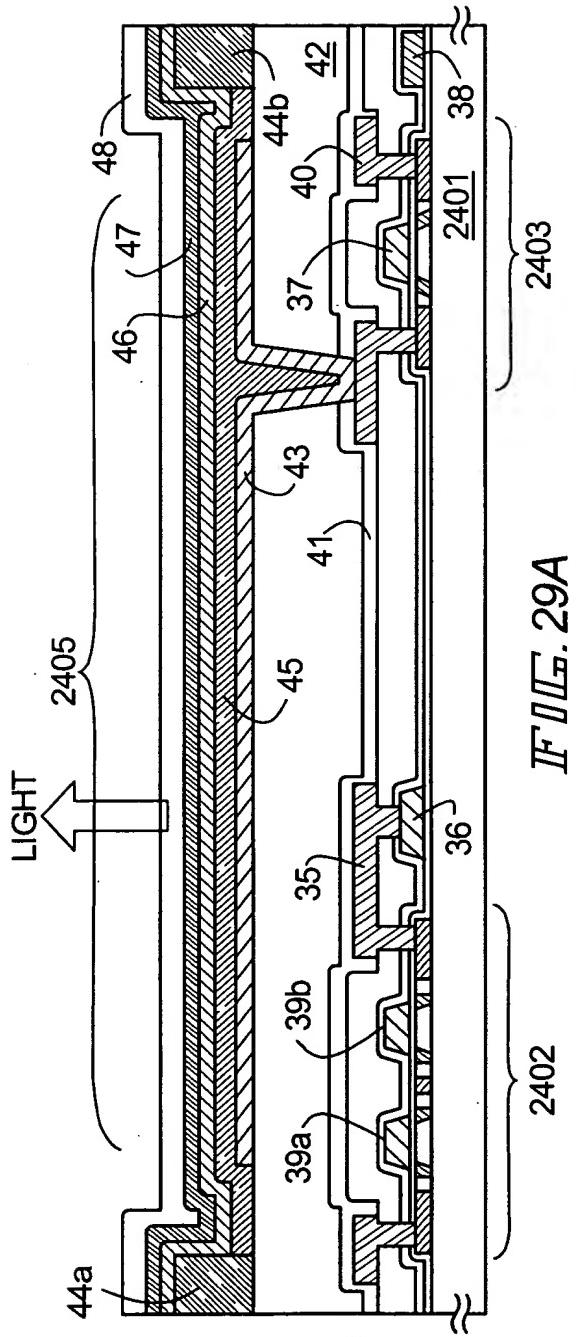


FIG. 29A

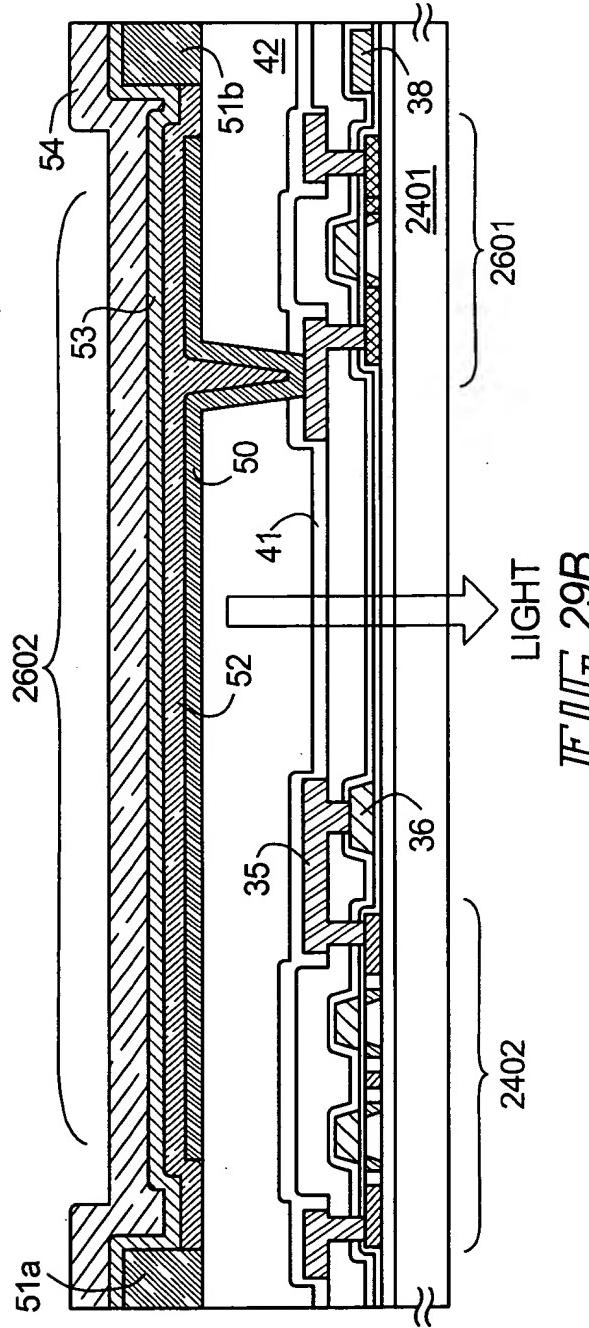


FIG. 29B

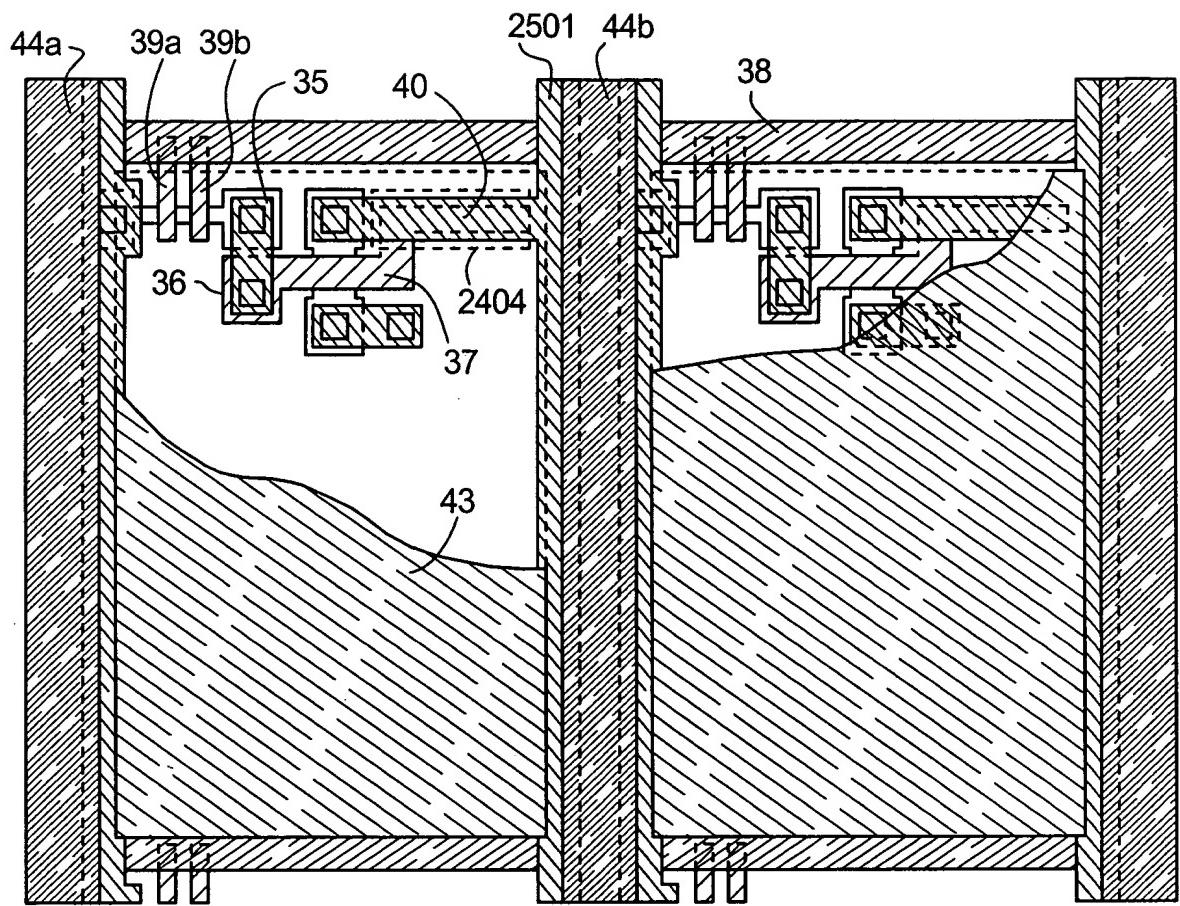


FIG. 30A

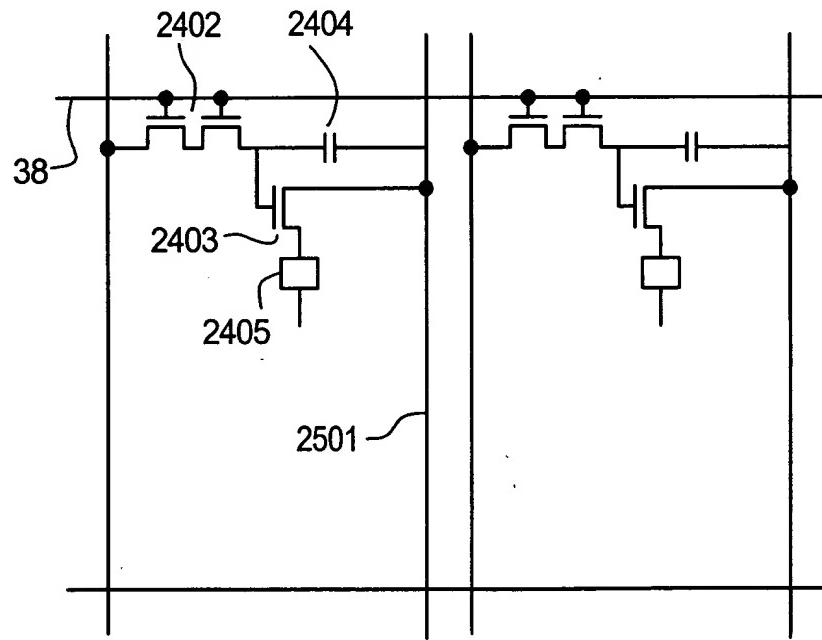


FIG. 30B

+

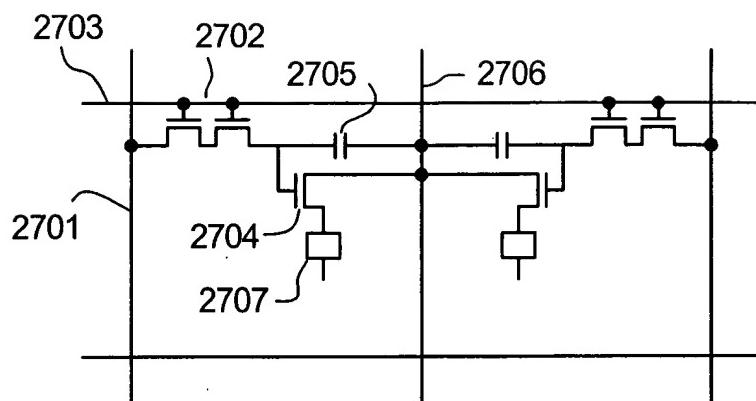


FIG. 31A

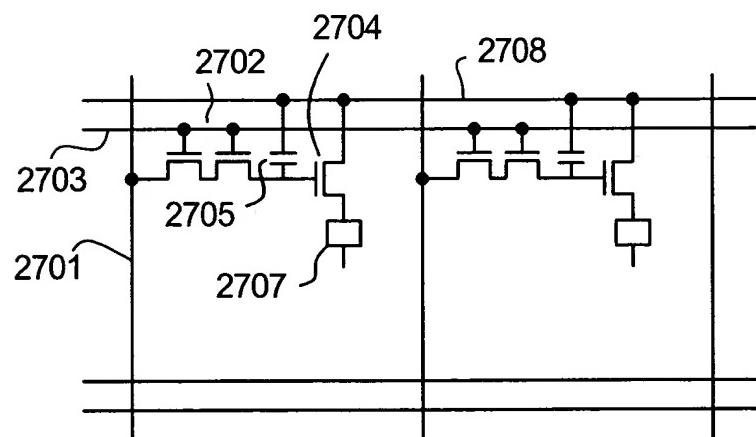


FIG. 31B

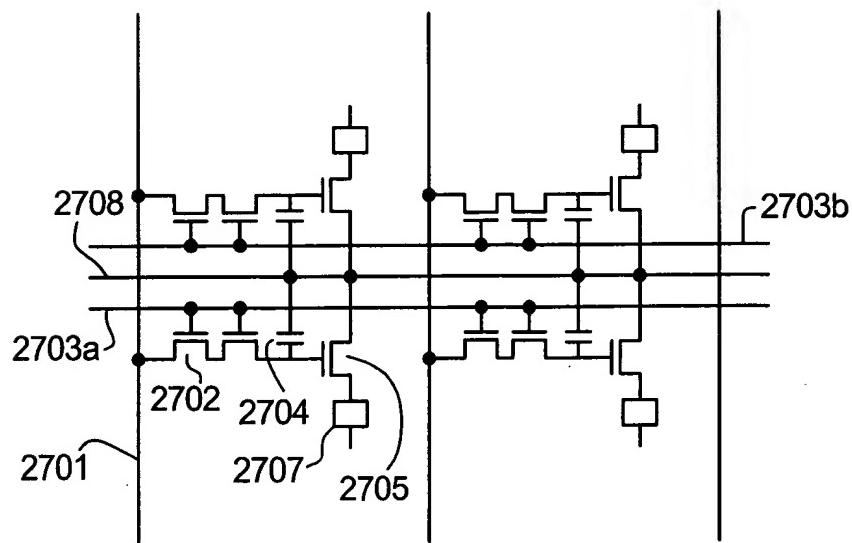


FIG. 31C

+

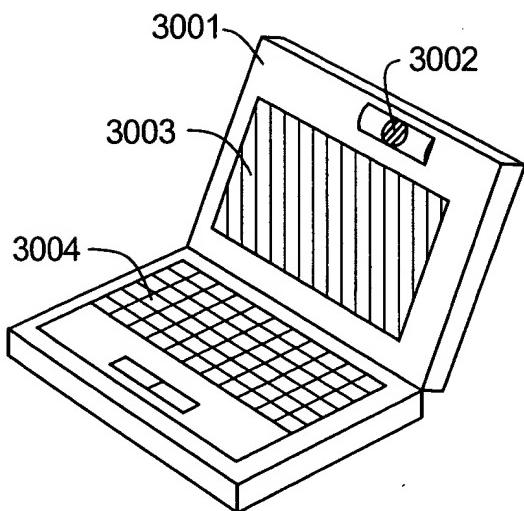


FIG. 32A

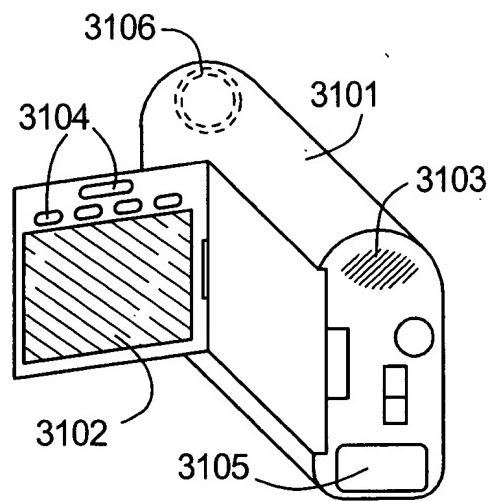


FIG. 32B

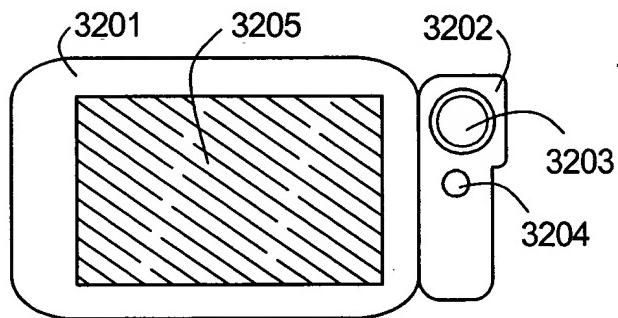


FIG. 32C

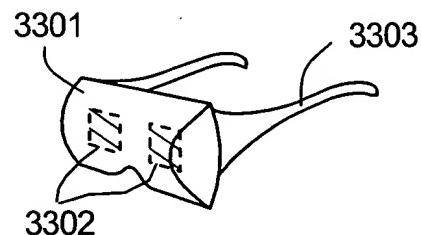


FIG. 32D

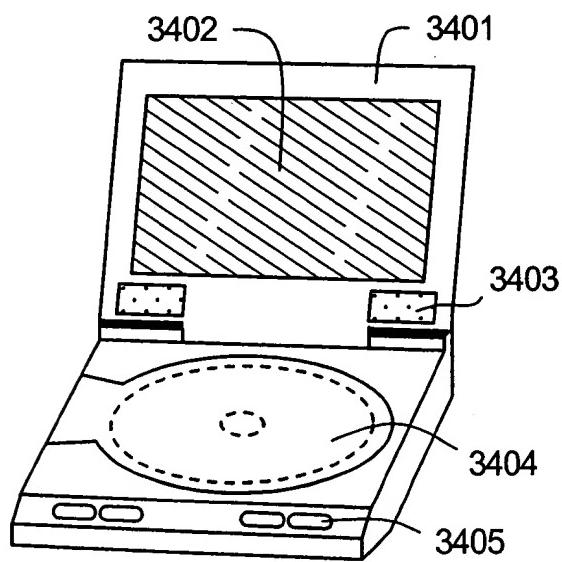


FIG. 32E

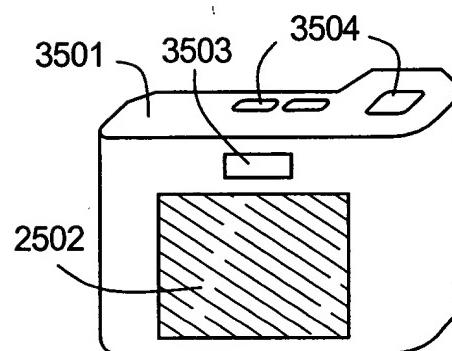


FIG. 32F

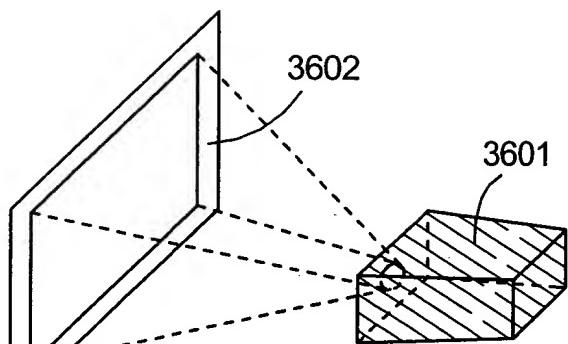


FIG. 33A

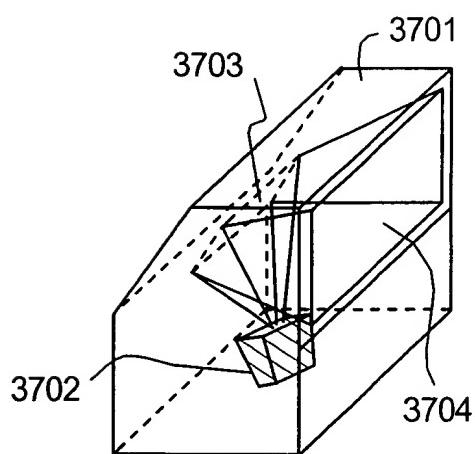


FIG. 33B

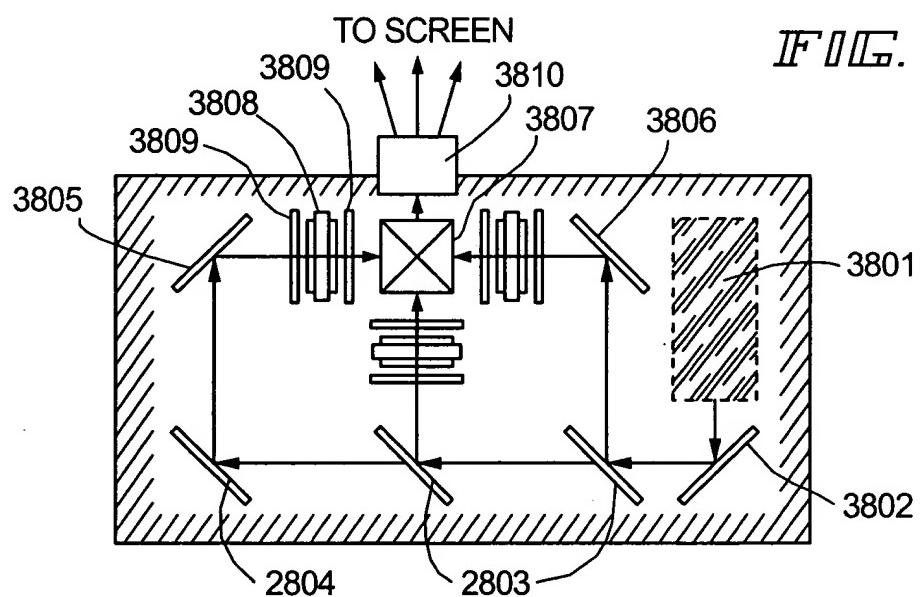


FIG. 33C

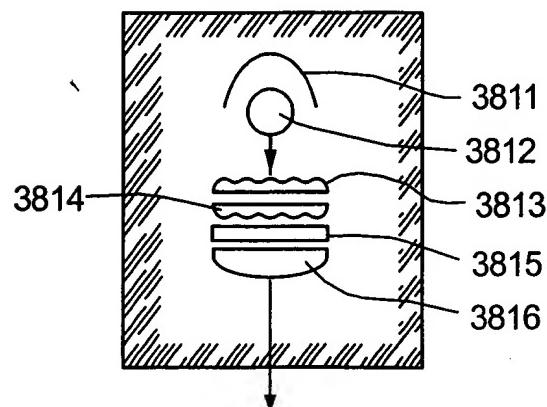


FIG. 33D

+

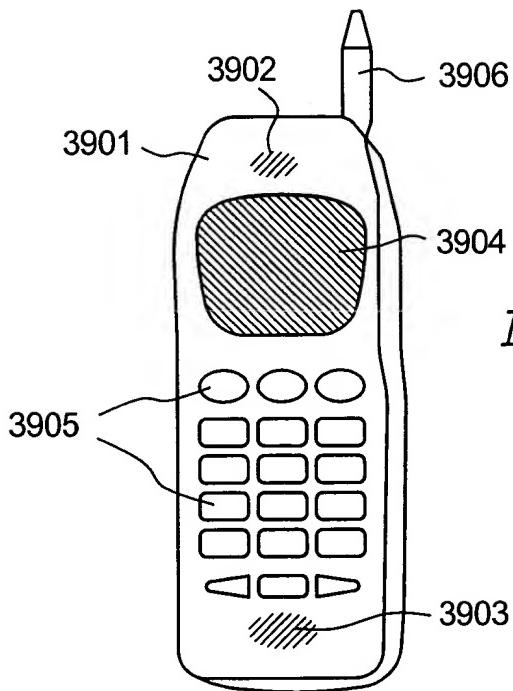


FIG. 34A

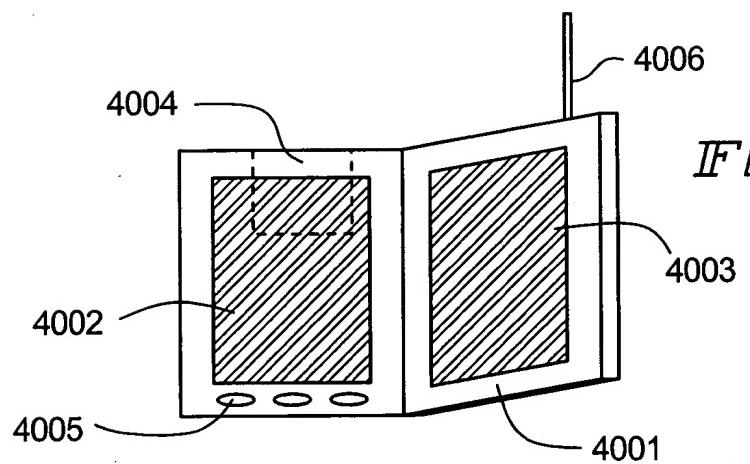


FIG. 34B

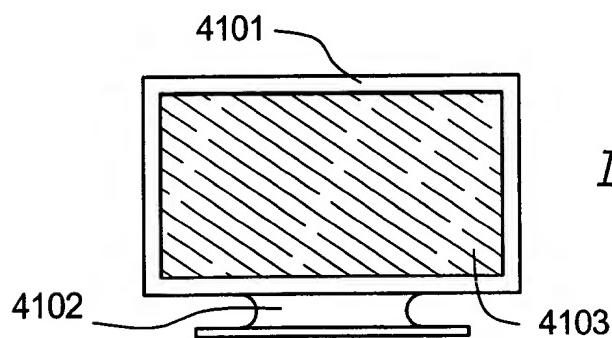


FIG. 34C

+

FIG. 35A

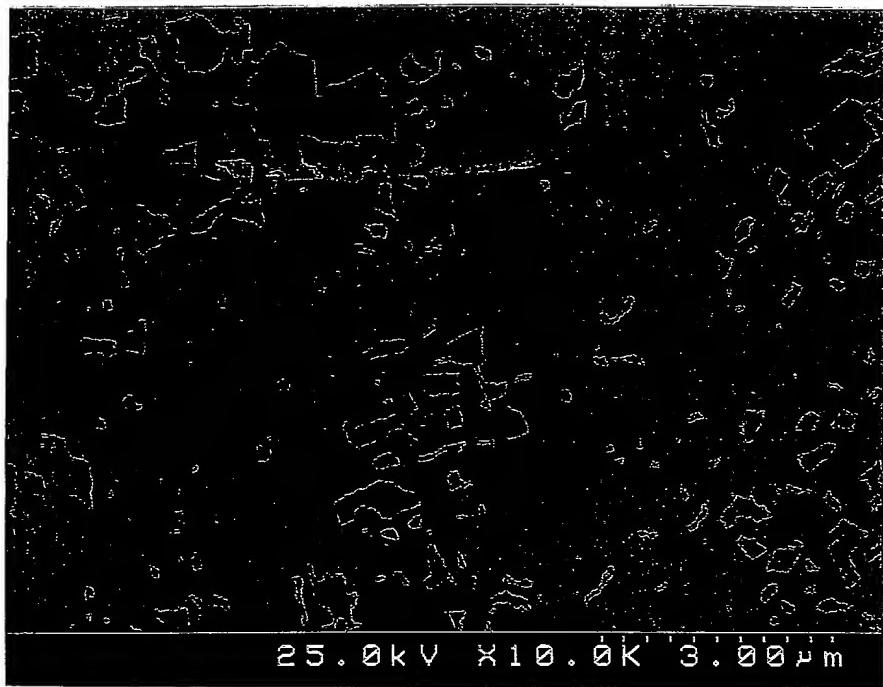


FIG. 35B

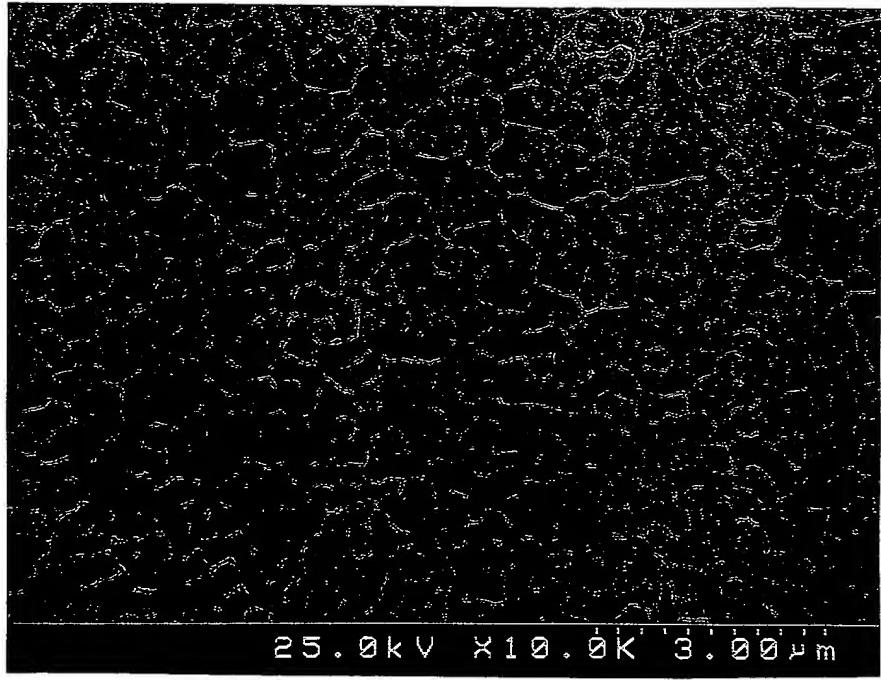


FIG. 36A

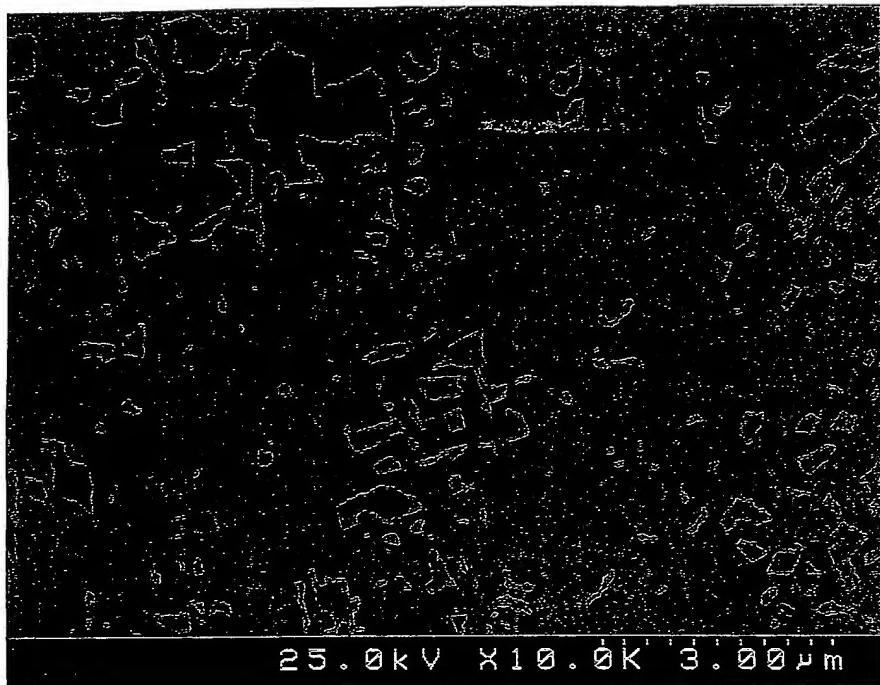


FIG. 36B

